

Exercises in *Nonstandard Static Analysis* of Hybrid Systems

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Abstract. In formal verification of hybrid systems, a big challenge is to incorporate continuous *flow* dynamics in a discrete framework. Our previous work proposed to use *nonstandard analysis* (NSA) as a vehicle from discrete to hybrid; and to verify hybrid systems using a Hoare logic. In this paper we aim to exemplify the potential of our approach, through transferring *static analysis* techniques to hybrid applications. The transfer is routine via the *transfer principle* in NSA. The techniques are implemented in our prototype automatic precondition generator.

1 Introduction

Hybrid systems exhibit both discrete (digital) *jump* and continuous (physical) *flow* dynamics. They are of paramount importance now that so many physical systems—cars, airplanes, etc.—are controlled with computers. For an approach to hybrid systems from the formal verification community, the challenge is: 1) to incorporate flow-dynamics; and 2) to do so at the lowest possible cost, so that the discrete framework smoothly transfers to hybrid situations. A large body of existing work uses *differential equations* explicitly in the syntax; see the discussion of related work below.

In [21], instead, we proposed to introduce a constant dt for an *infinitesimal* (i.e. infinitely small) value, and *turn flow into jump*. With dt , the continuous operation of integration can be represented by a while-loop, to which existing discrete techniques such as Hoare-style program logics readily apply. For a rigorous mathematical development we employed *nonstandard analysis* (NSA) beautifully formalized by Robinson [15].

Concretely, in [21] we took the common triple of a WHILE-language, a first-order assertion language and a Hoare logic (e.g. in the textbook [22]); and added a constant dt to obtain a modeling and verification framework for hybrid systems. Its three components are called WHILE^{dt} , ASSN^{dt} and HOARE^{dt} . These are connected by denotational semantics defined in the language of NSA. We proved soundness and relative completeness of the logic HOARE^{dt} . Underlying the technical development is the idea of what we call *sectionwise execution*, illustrated by the following example.

Example 1.1 Let c_{elapse} be the program on the right. The value of dt is infinitesimal; therefore the `while` loop will not terminate within finitely many steps. Nevertheless it is somehow intuitive to expect that after an “execution” of this program, the value of t should be infinitesimally close to 1.

```
t := 0 ;  
while t ≤ 1 do  
  t := t + dt
```

Our idea is to think about *sectionwise execution*. For each natural number i we consider the i -th section of the program c_{elapse} , denoted by $c_{\text{elapse}}|_i$ and shown on the right. Concretely, $c_{\text{elapse}}|_i$ is obtained by replacing the infinitesimal dt in c_{elapse} with $\frac{1}{i+1}$. Informally $c_{\text{elapse}}|_i$ is the “ i -th approximation” of the original c_{elapse} .

A section $c_{\text{elapse}}|_i$ does terminate within finite steps and yields $1 + \frac{1}{i+1}$ as the value of t . Now we collect the outcomes of sectionwise executions and obtain a sequence

$$(1 + 1, 1 + \frac{1}{2}, 1 + \frac{1}{3}, \dots, 1 + \frac{1}{i}, \dots) \quad (1)$$

which is thought of as a progressive approximation of the actual outcome of the original program c_{elapse} . Indeed, in the language of NSA, the sequence (1) represents a *hyperreal number* r that is infinitesimally close to 1.

We note that a program in WHILE^{dt} is *not* executable in general: the program c_{elapse} executes infinitely many iterations. It is however a merit of *static* approaches to verification, that programs need not be executed to prove their correctness. Instead, well-defined mathematical semantics suffices and supports deductive reasoning. This is what we do, with the denotational semantics of WHILE^{dt} exemplified in Example 1.1.

This paper is a first step towards serious use of our framework of [21]. We employ various discrete strategies for reasoning about programs—in Hoare-style logics, the concern is mostly *invariant discovery*—and verify small, but nontrivial, examples. Such discrete techniques are actively pursued in program verification, or, in the *static analysis* community. This paper aims to exemplify our framework’s potential for transferring static analysis techniques to hybrid applications. In it we rely on nonstandard analysis; hence our venture is called *nonstandard static analysis*. We have implemented a prototype that generates a precondition, given a program and a postcondition.

In what follows we present the strategies for simplification and invariant/precondition discovery that we implemented, and prove their correctness. In fact we only present the strategies’ *standard* version—i.e. for WHILE and HOARE , as opposed to the *nonstandard* version that is for WHILE^{dt} and HOARE^{dt} . This is because the transfer from the former to the latter is routine work. Soundness of the nonstandard version follows from that of the standard one, almost trivially via the *sectionwise lemmas*. We will demonstrate this process using one of the strategies, in §4.2.

A related issue is the legitimacy of use of *Mathematica* for symbolic computation in our prototype. What we prove in *Mathematica* are formulas about real numbers, not hyperreals; and this needs justification. In §5.2 we address this issue and show that proofs in *Mathematica* indeed prove formulas about hyperreal numbers. The key is the *transfer principle*, a celebrated result in NSA (see §2 later).

We defer most of the proofs to the appendix.

Related work. There have been extensive research efforts towards hybrid systems from the formal verification community. Unlike the current work where we turn flow into jump via dt , most of them feature acute distinction between flow- and jump-dynamics.

Model-checking approaches to hybrid systems have been studied for quite a while, with the successful formalism of *hybrid automaton* [1], on the one hand. On the other hand, deductive approaches have seen great advancement through a recent series of work by Platzer and his colleagues (including [12, 14]), resulting in the automated

prover KeYmaera. Our *nonstandard static analysis* approach currently falls much short of theirs in scalability and sophistication. However some of their techniques can be translated into the techniques in our framework; one example is the *differential invariant* strategy (§5.3). Interestingly in [14] it is argued that: being hybrid imposes no additional burden to verification in principle. This concurs with our claim.

Research in static analysis resulted in a huge number of verification techniques—[4, 7, 18] to name just a few. Some of them have been already used for hybrid applications (modeled with explicit differential equations) [16, 17, 19]. Our thesis is that these discrete techniques can be transferred to hybrid applications *as they are*, via NSA. In §4.1 we transfer the *phase split* technique in [20].

The use of NSA as a foundation of hybrid system modeling is not proposed for the first time; see e.g. [3, 5, 8]. Compared to this existing work, we claim our novelty is the use of NSA machinery (notably the transfer principle) in actual, automatic verification.

One recent research program (resulting e.g. in [6]) aims to employ *continuous* techniques—from the theory of dynamical systems—in purely discrete programs and applications. This is opposite to our approach (discrete techniques applied to continuous applications). We believe, however, that the two directions are not contending ones. It is not at all our intention here to champion the superiority of discrete techniques; our point instead is that the collection of available discrete techniques has much wider applicability. It is indeed our future work to *combine* discrete and continuous techniques.

2 Preliminaries

We summarize our previous work [21], focusing on providing intuitions. We denote the syntactic equality by \equiv .

Nonstandard Analysis. For detailed expositions see e.g. [9, 11].

We fix an *ultrafilter* $\mathcal{F} \subseteq \mathcal{P}(\mathbb{N})$ that extends the cofinite filter $\mathcal{F}_c := \{S \subseteq \mathbb{N} \mid \mathbb{N} \setminus S \text{ is finite}\}$. Its properties to be noted: 1) for any $S \subseteq \mathbb{N}$, exactly one of S and $\mathbb{N} \setminus S$ belongs to \mathcal{F} ; 2) if S is *cofinite* (i.e. $\mathbb{N} \setminus S$ is finite), then S belongs to \mathcal{F} .

Definition 2.1 (Hypernumber $d \in {}^*\mathbb{D}$) For a set \mathbb{D} (typically it is \mathbb{N} or \mathbb{R}), we define the set ${}^*\mathbb{D}$ by ${}^*\mathbb{D} := \mathbb{D}^{\mathbb{N}} / \sim_{\mathcal{F}}$. It is the set of infinite sequences on \mathbb{D} modulo the following equivalence $\sim_{\mathcal{F}}$: we define $(d_0, d_1, \dots) \sim_{\mathcal{F}} (d'_0, d'_1, \dots)$ by

$$\{i \in \mathbb{N} \mid d_i = d'_i\} \in \mathcal{F}, \quad \text{for which we say “}d_i = d'_i \text{ for almost every } i\text{.”}$$

Therefore, given that two sequences $(d_i)_i$ and $(d'_i)_i$ coincide except for finitely many indices i , they represent the same hypernumber. Other predicates (such as $<$) are defined in the same way. For example a hyperreal $\omega^{-1} := [(1, \frac{1}{2}, \frac{1}{3}, \dots)]$ is positive ($0 < \omega^{-1}$) but is smaller than any (standard) positive real $r = [(r, r, \dots)]$.

The framework of WHILE^{dt}, ASSN^{dt} and HOARE^{dt}. We take the standard combination (e.g. in [22]) of a while-style programming language WHILE, a first-order assertion language ASSN and a Hoare-style program logic HOARE (we equip them with constants for all real numbers). Then we augment the framework with a constant *dt* that denotes

a specific infinitesimal $\omega^{-1} = [(1, \frac{1}{2}, \frac{1}{3}, \dots)]$, and obtain the *nonstandard* framework consisting of WHILE^{dt} , ASSN^{dt} and HOARE^{dt} .

Definition 2.2 (WHILE^{dt} , WHILE , ASSN^{dt} , ASSN) The syntax of WHILE^{dt} is:

$$\begin{aligned} \mathbf{AExp} \ni \quad & a ::= x \mid r \mid a_1 \text{ aop } a_2 \mid \text{dt} \\ & \text{where } x \in \mathbf{Var}, r \text{ is a constant for } r \in \mathbb{R}, \text{ and aop} \in \{+, -, \cdot, /, [_]\} \\ \mathbf{BExp} \ni \quad & b ::= \text{true} \mid \text{false} \mid b_1 \wedge b_2 \mid \neg b \mid a_1 < a_2 \\ \mathbf{Cmd} \ni \quad & c ::= \text{skip} \mid x := a \mid c_1; c_2 \mid \text{assert } b \mid \text{if } b \text{ then } c_1 \text{ else } c_2 \mid \text{while } b \text{ do } c \end{aligned}$$

Here \mathbf{AExp} is the set of *arithmetic expressions*; \mathbf{BExp} and \mathbf{Cmd} are those of *Boolean* and *command* expressions. The operator $[_]$ denotes the smallest natural number which is larger than or equal to the real number r . We will use this operator in §5.4. The command $\text{assert } b$ amounts to skip if b is true; an infinite loop (divergence) otherwise.

Our first-order assertion language³ ASSN^{dt} consists of the following *formulas*.

$$\begin{aligned} \mathbf{Fml} \ni A ::= \quad & \text{true} \mid \text{false} \mid A_1 \wedge A_2 \mid \neg A \mid a_1 < a_2 \mid \\ & \forall x \in {}^*\mathbb{N}. A \mid \forall x \in {}^*\mathbb{R}. A \quad \text{where } x \in \mathbf{Var} \text{ and } a_i \in \mathbf{AExp} \end{aligned}$$

By WHILE , we denote the fragment of WHILE^{dt} without the constant dt .

By ASSN we designate the language obtained from ASSN^{dt} by: 1) dropping the constant dt ; and 2) replacing the quantifiers $\forall x \in {}^*\mathbb{N}$ and $\forall x \in {}^*\mathbb{R}$ with $\forall x \in \mathbb{N}$ and $\forall x \in \mathbb{R}$, respectively, i.e. with those which range over standard numbers.

It is essential that in ASSN^{dt} we allow only *hyperquantifiers* $\forall x \in {}^*\mathbb{R}$ and not *standard* ones $\forall x \in \mathbb{R}$. This is much like with the *transfer principle* in NSA [11, Thm. II.4.5].

Definition 2.3 (Section $e|_i$) Let e be an expression of WHILE^{dt} or ASSN^{dt} , and $i \in \mathbb{N}$. The *i-th section* of e , denoted by $e|_i$, is obtained by: 1) replacing every occurrence of dt with the constant $1/(i+1)$; and 2) replacing every hyperquantifier $\forall x \in {}^*\mathbb{D}$ with $\forall x \in \mathbb{D}$. Here $\mathbb{D} \in \{\mathbb{N}, \mathbb{R}\}$. Obviously a section $e|_i$ is an expression of WHILE or ASSN .

Definition 2.4 (HOARE^{dt} , HOARE) HOARE^{dt} is a system that derives *Hoare triples* $\{A\}c\{B\}$ (a triple of ASSN^{dt} formulas A, B and a WHILE^{dt} command c) using the following rules. The rules are the same for HOARE .

$$\begin{array}{c} \frac{}{\{A\} \text{skip} \{A\}} \text{ (SKIP)} \qquad \frac{}{\{A[a/x]\} x := a \{A\}} \text{ (ASSIGN)} \\ \frac{\{A\} c_1 \{C\} \quad \{C\} c_2 \{B\}}{\{A\} c_1; c_2 \{B\}} \text{ (SEQ)} \qquad \frac{\{A \wedge b\} c_1 \{B\} \quad \{A \wedge \neg b\} c_2 \{B\}}{\{A\} \text{if } b \text{ then } c_1 \text{ else } c_2 \{B\}} \text{ (IF)} \\ \frac{\{A\} c_1; c_2 \{B\} \quad \{A \wedge b\} c \{A\}}{\{A\} \text{while } b \text{ do } c \{A \wedge \neg b\}} \text{ (WHILE)} \qquad \frac{\models A \Rightarrow A' \quad \{A'\} c \{B'\} \quad \models B' \Rightarrow B}{\{A\} c \{B\}} \text{ (CONSEQ)} \\ \frac{}{\{b \Rightarrow A\} \text{assert } b \{A\}} \text{ (ASSERT)} \end{array}$$

We write $\vdash \{A\}c\{B\}$ if the triple $\{A\}c\{B\}$ can be derived using the above rules.

³ The term *assertion language* has little to do with the command $\text{assert } b$ in WHILE^{dt} .

We turn to semantics. Denotational semantics of WHILE^{dt} is defined following the intuition in Example 1.1. There the semantics $\llbracket c \rrbracket$ of a command c is a *hyperstate transformer* that maps a hyperstate σ (a state that stores hypernumbers) to a hyperstate $\llbracket c \rrbracket \sigma$.

Semantics of an assertion $A \in \mathbf{Fml}$ is defined in the usual way; we write $\models A$ and say A is *valid* if $\sigma \models A$ for each hyperstate σ . A Hoare triple is *valid* ($\models \{A\}c\{B\}$) if, for any hyperstate σ such that $\sigma \models A$, we have $\llbracket c \rrbracket \sigma \models B$.

Three *sectionwise lemmas* play central roles in our framework. They correspond to *Loś's theorem* in NSA. We only present the following one, that is the most relevant to the current paper. Recall the meaning of “for almost every i ” via an ultrafilter (§2).

Lemma 2.5 (Sectionwise validity of Hoare triples) *Let A, B be ASSN^{dt} formulas, and $c \in \mathbf{Cmd}$ be a WHILE^{dt} command. We have*

$$\models \{A\}c\{B\} \iff \models \{A|_i\} c|_i \{B|_i\} \text{ for almost every } i. \quad \square$$

Definition 2.6 (*-transform) Let A be an ASSN formula. We define its **-transform*, denoted by $*A$, to be the ASSN^{dt} formula obtained from A by replacing every occurrence of a standard quantifier $\forall x \in \mathbb{D}$ with the corresponding hyperquantifier $\forall x \in * \mathbb{D}$.

Proposition 2.7 (Transfer principle) *1. For each ASSN formula A , $\models A$ iff $\models *A$.
2. For any dt-free ASSN^{dt} formula A , the following are equivalent: 1) $\models A|_i$ for each $i \in \mathbb{N}$; 2) $\models A|_i$ for some $i \in \mathbb{N}$; 3) $\models A$. \square*

3 A Leading Example: ETCS

We use verification of (a simplified version of) the *European Train Control System (ETCS)* as a leading example—this is done also in many chapters of [13]. We go step by step, introducing the strategies as the need arises.

Our target program ETCS_0 is shown on the right. It contains small fragments of the European Train Control System (ETCS). Here a, t, v , and z are variables that represent acceleration rate, time, velocity, and position, respectively. The symbols $m, s, b, a0, \text{eps}$ are all constants, all of which are assumed to be (strictly) positive. Here, m is the position beyond which the train must not run (“a wall”); s is the *safety distance* such that, once the train’s distance from the wall is less than s , it starts braking; b is the rate used in braking; $a0$ is the (positive) acceleration rate used unless the train is braking. The check if $m - z < s$ occurs once every eps seconds.

The (post)condition to be guaranteed after the execution of ETCS_0 is the safety property $z < m$; it is inserted in Fig. 1 as an annotation, between $(: \text{ and } :)$. We set out to calculate a precondition—a relationship among constants as well as (the initial values of) the variables—that ensures this postcondition.

The program ETCS_0 is itself a while-loop; thus to calculate a precondition we need to discover a (loop) invariant. However the loop is a complicated one with an if branch and another while-loop inside. We first preprocess ETCS_0 and simplify it, employing some program transformation strategies studied in the field of static analysis.

Fig. 1. The original program ETCS_0

```
while (v > 0) {
  if m - z < s
    then a := -b
    else a := a0;
  t := 0;
  while (t < eps && v > 0) {
    z := z + v * dt;
    v := v + a * dt;
    t := t + dt }
  (: z < m :)
```

4 Program Simplification Strategies

4.1 Phase Split

Looking at ETCS₀ (Fig. 1), we can imagine that once the train starts braking, it never starts to accelerate. That is, we could transform the outer loop in ETCS₀ into two successive loops, with the former loop solely for accelerating and the latter loop for braking.

It is the program transformation technique proposed in [20] (closely related to those in [2, 10]) that makes the above intuition rigorous. It eliminates an if-branch inside a while loop using a *phase splitter predicate* b_s , as is shown on the right. This simplification strategy shall be referred to as *phase split*.

$$\text{while } b_g \text{ do } \boxed{\dots (\text{if } \dots) \dots} \\ \text{into } \left[\begin{array}{l} \text{while } b_g \wedge \neg b_s \text{ do } \boxed{\dots} \\ \text{while } b_g \wedge b_s \text{ do } \boxed{\dots} \end{array} \right];$$

First we follow [20] and describe the strategy formalized in WHILE and HOARE. In §4.2 we use the sectionwise lemmas and show that the nonstandard framework (WHILE^{dt} and HOARE^{dt}), too, admits the same strategy.

Definition 4.1 (Holed command, pre-hole fragment) The set $\mathbf{Cmd}_{[_]}$ of *holed commands* of WHILE—those which contain exactly one hole $[_]$ for a guard of if—is defined by the BNF expression below. Here $c, c_1, c_2 \in \mathbf{Cmd}$ are commands (without holes). The result of replacing $[_]$ with $b \in \mathbf{BExp}$ in $h \in \mathbf{Cmd}_{[_]}$ is denoted by $h[b]$.

$$\begin{aligned} \mathbf{Cmd}_{[_]} \ni h &::= \text{if } [_] \text{ then } c_1 \text{ else } c_2 \mid h; c \mid c; h \mid \\ &\quad \text{if } b \text{ then } h \text{ else } c \mid \text{if } b \text{ then } c \text{ else } h \\ \overline{\text{if } [_] \text{ then } c_1 \text{ else } c_2} &::= \text{skip} \quad \overline{h}; c ::= \overline{h} \quad \overline{c}; \overline{h} ::= c; \overline{h} \\ \overline{\text{if } b \text{ then } h \text{ else } c} &::= \text{assert } b; \overline{h} \quad \overline{\text{if } b \text{ then } c \text{ else } h} ::= \text{assert } \neg b; \overline{h} \end{aligned}$$

For each holed command h , its *pre-hole fragment* \overline{h} is defined inductively as above. Intuitively, it is the fragment of h that is executed before hitting the hole $[_]$.

Lemma 4.2 (Phase split [20]) *If a Boolean expression $b_s \in \mathbf{BExp}$ satisfies*

$$\models \{b_s\} \overline{h} \{b_c\}, \quad \models \{\neg b_s\} \overline{h} \{\neg b_c\}, \quad \text{and} \quad \models \{b_g \wedge b_s\} h[b_c] \{\neg b_g \vee b_s\}, \quad (2)$$

then we have $\llbracket c_0 \rrbracket = \llbracket c_1 \rrbracket$ between the commands

$$\begin{aligned} c_0 &::= \text{while } b_g \text{ do } h[b_c], \quad \text{and} \\ c_1 &::= \left[\text{while } (b_g \wedge \neg b_s) \text{ do } h[\text{false}]; \text{while } (b_g \wedge b_s) \text{ do } h[\text{true}] \right]. \quad (3) \quad \square \end{aligned}$$

Such b_s is called a *phase splitter predicate*. In applying this lemma to ETCS₀, an obvious candidate for a phase splitter predicate is $m - z < s$, the guard of the if-branch itself. (Its negation $m - z \geq s$ is a candidate too; but the side conditions (2) cannot be discharged.)

To discharge the side conditions (2) is not hard: in its course we use the *differential invariant* strategy described later in §5.3. We further apply obvious simplifications, such as $\llbracket \text{if true then } c_1 \text{ else } c_2 \rrbracket = \llbracket c_1 \rrbracket$ and constant propagation; consequently we are led to the program ETCS₁ (on the right) that is equivalent to ETCS₀.

Fig. 2. The program ETCS₁

```
while (v > 0 && m - z >= s) {
  a := a0; t := 0;
  while (t < eps && v > 0) {
    z := z + v * dt;
    v := v + a0 * dt;
    t := t + dt;
  };
  while (v > 0 && m - z < s) {
    a := -b; t := 0;
    while (t < eps && v > 0) {
      z := z + v * dt;
      v := v - b * dt;
      t := t + dt;
    };
  };
}
(: z < m :)
```

4.2 From Standard to Nonstandard I: Modular Transfer

As stated in the introduction, the current paper presents strategies (for simplification and invariant/precondition discovery) only for the standard framework (WHILE and HOARE), leaving out the corresponding nonstandard strategies (for WHILE^{dt} and HOARE^{dt}). This is because the transfer from the former to the latter is straightforward. Here we describe the process of such transfer, with the phase split strategy (§4.1) as an example. Here the *sectionwise lemmas* (in particular Lem. 2.5) play the key role.

The syntactic notions of holed command and pre-hole fragment are defined in WHILE^{dt}, in the exactly the same way as in WHILE (Def. 4.1).

Lemma 4.3 (Nonstandard phase split) *Assume $b_s \in \mathbf{BExp}$ satisfies, in HOARE^{dt},*

$$\models \{b_s\} \bar{h} \{b_c\} \ , \quad \models \{\neg b_s\} \bar{h} \{\neg b_c\} \ , \quad \text{and} \quad \models \{b_g \wedge b_s\} h[b_c] \{\neg b_g \vee b_s\} \ .$$

Let c_0 and c_1 be as in (3), now in WHILE^{dt}. We have $\llbracket c_0 \rrbracket = \llbracket c_1 \rrbracket$. □

Proof. Let $\sigma \in \mathbf{HSt}$ be a hyperstate; and $(\sigma|_i)_{i \in \mathbb{N}}$ be its arbitrary sequence representation. By the definition of hypernumbers, it suffices to show that

$$\llbracket c_0|_i \rrbracket(\sigma|_i) = \llbracket c_1|_i \rrbracket(\sigma|_i) \quad \text{for almost every } i. \quad (4)$$

By the assumptions and Lem. 2.5, each one of the following holds for almost every i .

$$\models \{b_s|_i\} \bar{h}|_i \{b_c|_i\} \models \{\neg b_s|_i\} \bar{h}|_i \{\neg b_c|_i\} \models \{b_g \wedge b_s|_i\} h[b_c]|_i \{\neg b_g \vee b_s|_i\} \ .$$

Therefore, by the definition of ultrafilter, all three of the above hold simultaneously, for almost every i . That is, for almost every i , we have all of the following three hold:

$$\models \{b_s|_i\} \bar{h}|_i \{b_c|_i\} \models \{\neg(b_s|_i)\} \bar{h}|_i \{\neg(b_c|_i)\} \models \{b_g|_i \wedge b_s|_i\} h|_i[b_c|_i] \{\neg(b_g|_i) \vee b_s|_i\}$$

where we also used an obvious fact $\bar{h}|_i \equiv \overline{(h|_i)}$. To each such i we apply the standard version of the strategy (Lem. 4.2) and obtain, for almost every i ,

$$\begin{aligned} & \llbracket \text{while } b_g|_i \text{ do } h|_i[b_c|_i] \rrbracket(\sigma|_i) \\ &= \llbracket \text{while } (b_g|_i \wedge \neg b_s|_i) \text{ do } h|_i[\text{false}] ; \text{ while } (b_g|_i \wedge b_s|_i) \text{ do } h|_i[\text{true}] \rrbracket(\sigma|_i) \end{aligned}$$

Using another obvious fact that $h[b_s]|_i \equiv (h|_i)[b_s|_i]$, this is equivalent to (4). □

Note the *modularity* in the proof: the standard version's proof (Lem. 4.2) is completely hidden. For any other (standard) strategy presented in this paper, its nonstandard version follows in the same way via sectionwise arguments.

4.3 Superfluous Guard Elimination

In ETCS₁ each while-loop has its guard consisting of two atomic inequalities. This causes problems with our toolkit for invariant/precondition discovery, especially with the *counting iterations* strategy (§5.4). In fact, some of the conditions are seemingly superfluous: for example, in the second (internal) while-loop, $m - z < s$ is an invariant.

Lemma 4.4 (Superfluous guard elimination) *For commands*

$$\begin{aligned} c_0 & \equiv \text{while } (b \wedge b_{\text{sf}}) \text{ do } c, \text{ and} \\ c_1 & \equiv \text{if } b_{\text{sf}} \text{ then } (\text{while } b \text{ do } c) \text{ else skip,} \end{aligned}$$

if we have $\models \{b \wedge b_{\text{sf}}\} c \{b_{\text{sf}}\}$, then $\llbracket c_0 \rrbracket = \llbracket c_1 \rrbracket$. □

This strategy, together with some straightforward simplifications, transforms ETCS_1 into ETCS_2 shown below. For each of the three instances of the strategy, the side condition is easily discharged using the *differential invariant* strategy (§5.3).

The program	<pre>if (v > 0) then while (m - z >= s) { a := a0; t := 0; while (t < eps) { z := z + v * dt; v := v + a0 * dt; t := t + dt }} else skip;</pre>	<pre>while (v > 0) { a := -b; t := 0; while (t < eps && v > 0) { z := z + v * dt; v := v - b * dt; t := t + dt }} (: z < m :)</pre>
ETCS_2 :		

4.4 Time Elapse

The challenge in verifying WHILE programs is in while-loops. The second half of ETCS_2 is a loop with another loop inside; we wish to simplify this.

A close look reveals that the inside loop is vacuous: since the inside guard and the outside guard share the same condition $v > 0$, the condition $t < \text{eps}$ has in fact no effect.⁴ We often encounter this situation in the verification of WHILE^{dt} programs—other simplification strategies easily lead to such vacuous nested loops.

Lemma 4.5 (Time elapse) *Let t be a variable that does not occur in $b \in \text{BExp}$ or $c \in \text{Cmd}$; ε be a positive constant; and*

$$\begin{aligned} c_0 & \equiv \text{while } b \text{ do } (t := 0; \text{ while } (t < \varepsilon \wedge b) \text{ do } (t := t + \text{dt}; c)) , \\ c_1 & \equiv \text{while } b \text{ do } c . \end{aligned}$$

Then the denotations $\llbracket c_0 \rrbracket$ and $\llbracket c_1 \rrbracket$ coincide on all variables but t . □

Lem. 4.5 allows several straightforward generalizations. Currently we do not need them.

Since t does not occur in the postcondition $z < m$, Lem. 4.5 ensures that *time elapse* is a sound transformation. It simplifies the second half of ETCS_2 into ETCS_3 below.

The program	<pre>if (v > 0) then while (m - z >= s) { a := a0; t := 0; while (t < eps) { z := z + v * dt; v := v + a0 * dt; t := t + dt }} else skip;</pre>	<pre>while (v > 0) { a := -b; z := z + v * dt; v := v - b * dt } (: z < m :)</pre>
ETCS_3 :		

⁴ This is not the case with the first half of ETCS_2 : it might be $m - z < s$ but still the train can accelerate for eps seconds. Therefore the nested loops cannot be suppressed so easily.

5 Precondition/Invariant Discovery Strategies

We now describe three strategies aimed at the discovery of suitable preconditions/invariants for while-loops. In fact they return directly a precondition (when they succeed).

We desire their output to be quantifier-free, since quantifiers often incur prohibiting performance penalties. This is what we do in our prototype: quantifiers appear only in the *Mathematica* backend. In what follows several quantifiers do occur; but they are in the correctness proofs of the strategies, i.e. on the meta level.

5.1 Invariant via Quantifier Elimination

We now compute a precondition `precond1` that, after the execution of the last while-loop in ETCS_3 (which we denote by c_{brake}), ensures the postcondition $z < m$ hold.

Recall our thesis: via nonstandard analysis, we can represent continuous flow by discrete jumps, so that (discrete) verification techniques readily apply. However, once we cast a *continuous* look at c_{brake} , it obviously represents a simple flow dynamics governed by the differential equations $\dot{v}(t) = \ddot{z}(t) = -b$. In this “continuous” perspective, one would solve the current question in the following way.

- First the differential equations are solved analytically, obtaining $v(t) = v_0 - bt$ and $z(t) = z_0 + v_0t - \frac{1}{2}bt^2$. Here v_0 and z_0 are initial values.
- Using these analytic solutions, the desired precondition is nothing other than

$$\forall t \in \mathbb{R}. \left(v_0 - bt > 0 \wedge t \geq 0 \implies z_0 + v_0t - \frac{1}{2}bt^2 < m \right). \quad (5)$$

One would then apply *quantifier elimination* and obtain a quantifier-free formula that is equivalent to (5). Some algorithms are known including *cylindrical algebraic decomposition (CAD)*. In *Mathematica* `Resolve` offers this functionality; it returns

$$v_0 \leq 0 \vee \left(z_0 < m \wedge 2bm - v_0^2 - 2bz_0 \geq 0 \right).$$

This procedure of “first solve differential equations, then eliminate quantifiers” is common in the deductive verification of hybrid systems, such as in Platzer’s [13].

The next strategy (Lem. 5.2) is a discrete variation of this procedure, where differential equations are replaced with *difference equations* (also called *recurrence relations*).

We need preparation. In Lem. 5.2 we need to have a formula $A \overbrace{[a/x][a/x] \cdots [a/x]}^{y \text{ times}}$ as a formula *with y as a free variable*. This is not automatic—note that substitution $[a/x]$ and the number y live on the meta level. However, let us say that A is a formula $A \equiv (x > 0)$ and $a \equiv x + 1$. Then we have, for any $y \in \mathbb{N}$,

$$\models \quad (x > 0) [x + 1/x] [x + 1/x] \cdots [x + 1/x] \quad \Leftrightarrow \quad x + y > 0,$$

where substitution is repeated y times. Thus the formula $x + y > 0$ *effectively* represents the formula $(x > 0) [x + 1/x] [x + 1/x] \cdots [x + 1/x]$. In other words, $x + y > 0$ is a *homogeneous representation* of substitutions $x > 0, x + 1 > 0, (x + 1) + 1 > 0, \dots$

The general definition is as follows. There A_{rs} corresponds to $x + y > 0$ in the above example; it is a formula that must be discovered somehow. In our prototype it is done by solving a recurrence relation.

Definition 5.1 (Repeated substitution) Let x be a variable, A be a ASSN formula, a be an arithmetic expression, and y be a variable not occurring in A , a or x . A formula A_{rs} is said to be a *homogeneous representation* of A and $[a/x]$ if the following holds.

$$\models A_{\text{rs}}[0/y] \Leftrightarrow A \quad \wedge \quad \forall u \in \mathbb{N}. (A_{\text{rs}}[u+1/y] \Leftrightarrow (A_{\text{rs}}[u/y])[a/x]) \quad (6)$$

We abuse notations by denoting such A_{rs} by $A[a/x]^y$.

Lemma 5.2 (QE invariant)

$$\models \left\{ \begin{array}{l} (\neg b \Rightarrow A) \quad \wedge \\ \forall y \in \mathbb{N}. ((b[a/x]^y \wedge \neg b[a/x]^{y+1}) \Rightarrow A[a/x]^{y+1}) \end{array} \right\} \text{ while } b \text{ do } x := a \{A\} .$$

The lemma is presented in the simplest form: it is straightforward to allow a sequence $x_1 := a_1; \dots; x_n := a_n$ of assignments inside the loop.

Proof. Let P be the precondition $(\neg b \Rightarrow A) \wedge \forall y \in \mathbb{N}. ((b[a/x]^y \wedge \neg b[a/x]^{y+1}) \Rightarrow A[a/x]^{y+1})$. By the (WHILE) rule of HOARE, it suffices to show that: 1) P is a loop invariant, that is, $\models \{P \wedge b\}x := a\{P\}$; and 2) P is strong enough, i.e. $\models P \wedge \neg b \Rightarrow A$.

To prove 1), assume $\sigma \models P \wedge b$. The goal is $\llbracket x := a \rrbracket \sigma \models P$, which is equivalent to $\sigma \models P[a/x]$, since $P[a/x]$ is the weakest precondition for $x := a$ and the postcondition P . This is further reduced to: $\sigma \models b[a/x]^{n+1} \wedge \neg b[a/x]^{n+2} \Rightarrow A[a/x]^{n+2}$ for any $n \in \mathbb{N}$ (which follows from $\sigma \models P$); and $\sigma \models \neg b[a/x] \Rightarrow A[a/x]$ (which follows from $\sigma \models b$ and $\sigma \models (b \wedge \neg b[a/x]) \Rightarrow A[a/x]$).

The condition 2) is obvious since $\models P \Rightarrow (\neg b \Rightarrow A)$. This concludes the proof. \square

In our prototype, we first compute homogeneous representations $b[a/x]^y$ and $A[a/x]^y$ by solving recurrence relations in y such as $A[a/x]^{y+1} = (A[a/x]^y)[a/x]$. Using this we form the precondition in Lem. 5.2. Then we further eliminate the quantifier $\forall y \in \mathbb{N}$ and obtain a quantifier-free precondition.

For example, in the case of c_{brake} in ETCS₃, we discover a homogeneous representation $A[\vec{a}/\vec{x}]^y := \frac{1}{2}(b\text{dt}^2y + 2\text{dt}vy - b\text{dt}^2y^2 + 2z) < m$. As a quantifier-free precondition we obtain the following, which is henceforth denoted by P_1 .

$$P_1 := (v > 0 \vee m > z) \wedge (b^2\text{dt}^2 + 4b\text{dt}v + 8bz + 4v^2 < 8bm \vee b\text{dt}v + 2bz + v^2 \leq 2bm) \quad (7)$$

This strategy (Lem. 5.2) has the merit of having no side conditions. But in our experience it is computationally expensive: a slightly complicated postcondition (say, a combination of a few inequalities) makes the quantifier elimination step infeasible. In fact it does not currently work for the loops in ETCS₃ other than the last one c_{brake} .

5.2 From Standard to Nonstandard II: The Transfer Principle and Symbolic Computation in *Mathematica*

Here again we demonstrate how a standard strategy can be transferred to a nonstandard one. The strategy *QE invariant* (Lem. 5.2) employs *Mathematica* in two stages: solving recurrence relations and eliminating quantifiers, both done over standard numbers (instead of hyperreals). Our focus is therefore on how this can be justified.

Definition 5.3 (Repeated substitution in ASSN^{dt}) Let x, A, a, y be as in Def. 5.1, but now in ASSN^{dt} . A formula A'_{rs} is said to be a *homogeneous representation* of A and $[a/x]$, and is denoted by $A[a/x]^y$, if the following holds.

$$\models A'_{\text{rs}}[0/y] \Leftrightarrow A \quad \wedge \quad \forall u \in {}^*\mathbb{N}. (A'_{\text{rs}}[u+1/y] \Leftrightarrow (A'_{\text{rs}}[u/y])[a/x]) \quad (8)$$

The sole difference from Def. 5.1 is the quantifier (ranging over hypernatural numbers). How do we find A'_{rs} that satisfies (8)? We rely on the following lemma.

Lemma 5.4 *In the setting of Def. 5.3, let A' and a' be the expressions obtained from A and a by replacing the constant dt by a fresh variable d (and also by making hyperquantifiers standard). If A_{rs} is a homogeneous representation $A'[a'/x]^y$ in ASSN (Def. 5.1), then ${}^*A_{\text{rs}}[\text{dt}/d]$ is a homogeneous representation $A_{\text{rs}}[a/x]^y$ in ASSN^{dt} . \square*

Therefore it essentially suffices to prove (6). This is in ASSN hence *Mathematica* is capable of proving it.

Lemma 5.5 (Nonstandard QE invariant) *We have, in HOARE^{dt} ,*

$$\models \left\{ \begin{array}{l} (\neg b \Rightarrow A) \wedge \\ \forall y \in {}^*\mathbb{N}. (b[a/x]^y \wedge \neg b[a/x]^{y+1}) \Rightarrow A[a/x]^{y+1} \end{array} \right\} \text{ while } b \text{ do } x := a \{A\} .$$

Proof. By Lem. 2.5, the following suffices: in HOARE ,

$$\models \left\{ \left((\neg b \Rightarrow A) \wedge \forall y \in {}^*\mathbb{N}. (b[a/x]^y \wedge \neg b[a/x]^{y+1}) \Rightarrow A[a/x]^{y+1} \right) \Big|_i \right\} \text{ while } b|_i \text{ do } \{A|_i\}$$

holds for almost every $i \in \mathbb{N}$. By Def. 5.3, Def. 5.1 and [21, Lem. 4.5], it is easy that $\models (A[a/x]^y)|_i \Leftrightarrow A|_i[a|_i/x]^y$ for almost every $i \in \mathbb{N}$. Therefore it suffices to show

$$\models \left\{ \begin{array}{l} (\neg b|_i \Rightarrow A|_i) \wedge \\ \forall y \in \mathbb{N}. (b|_i[a|_i/x]^y \wedge \neg b|_i[a|_i/x]^{y+1}) \Rightarrow A|_i[a|_i/x]^{y+1} \end{array} \right\} \text{ while } b|_i \text{ do } \{A|_i\}$$

for almost every i . The last statement (in HOARE) is Lem. 5.2 itself. \square

The second use of *Mathematica*—in quantifier elimination—is justified much like in Lem. 5.4, via the transfer principle.

5.3 Differential Invariant

Most of the simplification strategies in §4 come with side conditions to be discharged. In many such cases, the postcondition to be established is itself an invariant. An example is the condition $v > 0$ in the first half of ETCS_1 .

The following lemma provides an efficient method for proving such Hoare triples.⁵

⁵ The name is taken from Platzer's extensive treatment of a similar notion [13]. The correspondence is as follows: when the while-loop below represents continuous dynamics, the condition $a_c[a/x] < a_c$ means that the first derivative of a_c is negative.

Lemma 5.6 (Differential Invariant) *Assume that an arithmetic expression a_c satisfies $\models b \Rightarrow a_c[a/x] < a_c$. Then $\models \{a_c < 0\} \text{ while } b \text{ do } x := a \{a_c < 0\}$. \square*

In our current prototype a candidate for the invariant $a_c < 0$ is chosen simply out of the disjuncts of the postcondition. More sophisticated candidate generation methods—such as in [13]—are left as future work.

5.4 Iteration Count

Verification of the leading example ETCS (§3) has been reduced, in §5.1, to the situation below on the left. Here postcondition P_1 is as in (7). Our prototype further simplifies the situation into the one on the right.

$$\begin{array}{l}
 \text{if } (v > 0) \\
 \text{then} \\
 \quad \text{while } (m - z \geq s) \{ \\
 \quad \quad a := a0; \quad t := 0; \\
 \quad \quad \text{while } (t < \text{eps}) \{ \\
 \quad \quad \quad z := z + v * dt; \\
 \quad \quad \quad v := v + a0 * dt; \\
 \quad \quad \quad t := t + dt \} \\
 \quad \quad \text{else skip} \\
 \quad (: P_1 :)
 \end{array}
 \quad \Bigg| \quad
 \begin{array}{l}
 \text{while } (m - z \geq s) \{ \\
 \quad a := a0; \quad t := 0; \\
 \quad \text{while } (t < \text{eps}) \{ \\
 \quad \quad z := z + v * dt; \\
 \quad \quad v := v + a0 * dt; \\
 \quad \quad t := t + dt \} \\
 \quad (: P_2 :) \\
 \text{where } P_2 := b^2 dt^2 + 4bdtv + 8bz + \\
 4v^2 < 8bm \vee bdtv + 2bz + v^2 \leq 2bm
 \end{array}
 \tag{9}$$

We now face a nested loop to which none of the previous strategies applies. Our last strategy estimates the number of iterations in each loop—such as eps/dt for the inner loop⁶—and approximates commands by repeated applications of assignments. The strategy involves nontrivial side conditions but we experienced its wide applicability.

The formalization of the full *iteration count* strategy is cumbersome and buries its idea in overwhelming details. Here we only present the following restricted version that deals with a single loop. The full version that applies to nested loops is deferred to the appendix.

Lemma 5.7 *Let a_i be an arithmetic expression. Assume the following conditions.*

1. $\models \neg b[a/x]^{a_i}$
2. *For some fresh variable y , $\models \{\neg b[a/x]^{y+1}\} c \{\neg b[a/x]^y\}$*
3. $\models \forall u, v \in \mathbb{R}. ((a_i \leq u < v < a_i + 1 \wedge \neg b[a/x]^u) \Rightarrow \neg b[a/x]^v)$
4. *For some fresh variable y , $\models \{A[a/x]^{y+1}\} c \{A[a/x]^y\}$*
5. $\models \forall u, v \in \mathbb{R}. ((u < v \wedge A[a/x]^v) \Rightarrow A[a/x]^u)$

Then we have

$$\models \{A[a/x]^{a_i+1} \wedge a_i \geq 0\} \text{ while } b \text{ do } c \{A\} . \tag{10}$$

\square

Here are some intuitions. The expression a_i denotes an estimated number of iterations: for example, $a_i \equiv \text{eps}/dt$ for the inner loop on the right in (9). The estimation is done by solving the equation $a_i \cdot dt = \text{eps}$. We are also *simulating* execution of c by a

⁶ Note that the number eps/dt makes sense in NSA: it means $(i+1) \cdot \text{eps}$ in the i -th section.

substitution $[a/x]$; the conditions 2. and 4. assert that this simulation is sound. Note that these conditions are trivially satisfied if c is an assignment command $x := a$.

The conditions 3. and 5. impose suitable *monotonicity* requirements on the guard b and the postcondition A . Such monotonicity is needed for the following reason. The estimated number a_i of iterations need not be exactly some (hyper)natural number—it can lie between two (hyper)natural numbers, i.e. $\lceil a_i \rceil - 1 < a_i < \lceil a_i \rceil$. This is why we are *conservative* in (10); we use $a_i + 1$, instead of a_i , as a number of iterations. The monotonicity conditions (3. & 5.) ensure that this conservative approximation is sound.

The full version of this *iteration count* strategy (Lem. 5.7) succeeds in the remaining bit of the leading example (on the right in (9)). Its output is a Boolean combination of inequalities; we denote this formula by P_3 . It is complicated—not least due to occurrences of dt —and we would rather not write it down explicitly.

5.5 Cast to Shadow

We have established that $\models \{P_3\} \text{ETCS}_0 \{z < m\}$. As the last step, we wish to eliminate the occurrences of dt in the precondition P_3 . This results in a much simpler precondition. The intuition is: the condition $\varepsilon + dt < 0$ is implied by $\varepsilon < 0$ if ε is a constant denoting a standard real number. To put it in rigorous terms:

Lemma 5.8 (Cast to Shadow) *Let a be an arithmetic expression in WHILE^{dt} , d be a fresh variable, and $a[d/dt]$ be the expression in WHILE obtained by substituting d for dt in a . Assume the following.*

1. *The expression a is closed, that is, it has no occurrences of variables.*
2. *Let σ be a (standard) state that is not \perp ; it determines a function⁷ $f_\sigma : \mathbb{R} \rightarrow \mathbb{R}$ by $r \mapsto \llbracket a[d/dt] \rrbracket (\sigma[d \mapsto r])$. We assume that f_σ is continuous at $r = 0$ for any given $\sigma \neq \perp$. Here $\llbracket a[d/dt] \rrbracket$ is defined by denotational semantics [21, §3.2]; and $\sigma[d \mapsto r]$ is an updated state.*

Then $\models (a[d/dt])[0/d] < 0 \implies a < 0$. □

Note that, by the definition of $a[d/dt]$, the expression $(a[d/dt])[0/d]$ is the result of replacing dt in a with 0—i.e. of “eliminating dt .” In using the lemma, the closedness assumption (Cond. 1.) can be enforced by setting the initial values of variables as constants (that necessarily represent standard reals by the definition of WHILE^{dt}). The continuity assumption (Cond. 2.) in the lemma is most of the time satisfied since the arithmetic operations of WHILE are all continuous except for zero-division.

In nonstandard analysis, a hyperreal number r' that is not infinite has a unique (standard) real number r that is infinitesimally close to r' (we write $r \simeq r'$). Such r is called the *shadow* of r' ; hence the name of the strategy.

This strategy can be used for simplifying the precondition P_3 (or, more generally, a Boolean combination of inequalities) in the following way.

- First, P_3 is converted to CNF (or DNF; it does not matter): $\models P_3 \Leftrightarrow \bigwedge_i \bigvee_j L_{i,j}$.
Note that every literal $L_{i,j}$ occurs positively.

⁷ In general a partial function since zero-division might occur.

- Among all the literals $L_{i,j}$, the negative ones (like $L_{i,j} \equiv \neg(a_{i,j} < b_{i,j})$) is turned into a positive one (like $a_{i,j} \geq b_{i,j}$), by reversing inequalities.
- Each inequality is turned into the form of either $c_{i,j} < 0$ or $c_{i,j} \leq 0$. For example: we turn $a_{i,j} \geq b_{i,j}$ into $b_{i,j} - a_{i,j} \leq 0$. Thus we have obtained $\models P_3 \Leftrightarrow \bigwedge_i \bigvee_j c_{i,j} \triangleleft_{i,j} 0$, where each $\triangleleft_{i,j}$ is either $<$ or \leq .
- Obviously $\models c_{i,j} < 0 \Rightarrow c_{i,j} \triangleleft_{i,j} 0$; thus $\models \bigwedge_i \bigvee_j c_{i,j} < 0 \Rightarrow P_3$.
- Finally, to each inequality $c_{i,j} < 0$ we apply Lem. 5.8 (given that the side conditions are discharged). This establishes $\models \bigwedge_i \bigvee_j (c_{i,j}[d/dt])[0/d] < 0 \Rightarrow P_3$; thus we obtain a “simplified” precondition that has no occurrences of dt .

We note that thus obtained precondition $\bigwedge_i \bigvee_j (c_{i,j}[d/dt])[0/d] < 0$ is not necessarily equivalent to P_3 , but is stronger (i.e. less general). In our experience, however, the lost generality is most of the time marginal.

After all, the following is the (core part of the long and extensive) outcome of our prototype when it is fed with $ETCS_0$.

$$a_0(2\varepsilon\sqrt{2a_0(m-s-z_0)+v_0^2+b\epsilon^2+2m-2s-2z_0} + 2b\varepsilon\sqrt{2a_0(m-s-z_0)+v_0^2+a_0^2\epsilon^2+v_0^2} < 2bs$$

Here v_0 and z_0 are constants that designate the initial values of v and z , respectively.

6 Implementation and Experiments

Our prototype implementation takes a $WHILE^{dt}$ program and a postcondition as its input, and tries to calculate a precondition. The tool consists of the following components.

- A native verification condition generator implemented in OCaml. It combines the standard verification condition generation for Hoare-type logics (such as in [22]) and the previously described strategies. It relies on the *Mathematica* backend for most of the strategies as well as for (symbolic) arithmetic proofs.
- A *Mathematica* backend that provides functions for the strategies in §4–5. It also implements some proof strategies for arithmetic formulas.
- A Perl script that serves as an interface between the above two.

Our prototype employs the standard backward reasoning for Hoare-type logics. When it encounters a while loop, it tries the precondition discovery strategies in §5. If one or more of those strategies succeed and produce preconditions P_1, \dots, P_n , then our prototype continues with the disjunction $P_1 \vee \dots \vee P_n$ of those preconditions. If not, our prototype tries simplification strategies in §4; the order is *phase split*, *superfluous guard elimination* and then *time elapse*. If the simplification succeeds, it again tries the precondition discovery strategies. If not, our prototype reports failure.

Although we sometimes use numeric computations for finding counterexamples, the proofs are established in purely symbolic means. This is a crucial fact for the correctness guarantee via the transfer principle.

We have tested our prototype against the following $WHILE^{dt}$ programs:

- `etcs.while`: the ETCS example used throughout the paper, and

- `zeno.while`: the behavior of a bouncing ball.

We conducted both experiments on Fujitsu HX600 with Quad Core AMD Opteron 2.3GHz CPU and 32GB memory. We used *Mathematica* 7.0 for Linux x86 (64-bit).⁸

The current implementation is premature. The goals of the experiments have been: 1) the feasibility test of our methodology of nonstandard static analysis, and 2) to drive theoretical development of static analysis strategies suited for hybrid applications. Enhancing scalability and efficiency of the prototype is left as future work.

`etcs.while`. Our prototype completed precondition generation for `etcs.while` in 40.96 seconds (31.97 seconds in the user process and 8.99 in the kernel process), generating the precondition described in §5.5 as an outcome.

`zeno.while`. We have modeled the behavior of a bouncing ball in `WHILEdt` and run our prototype on the program. This program includes the following three parameters:

- t_0 : ending time of the system;
- b : elastic coefficient between the ground and the ball;
- h_0 : *safety height*—the ball should not reach higher than this height.

The postcondition is that the peak height of the last bounce is not higher than h_0 .

Although the current prototype does not fully succeed to compute a precondition, it manages to simplify the program substantially. For example, the ascending and descending phases in the movement of the ball are discovered automatically, using Lem. 4.2. With a manual insertion of one condition (an inequality), our prototype runs through and outputs a symbolic precondition (which is too long to present here).

Though this bouncing ball example was not fully-automatic, we still find its result interesting and encouraging. The example exhibits the *Zeno behavior*, a big challenge for many verification techniques (see e.g. [13, Section 3.3.3]). We expect our framework to be useful in Zeno-type examples, since: 1) it does not distinguish flow dynamics from jump dynamics (dynamics is always “discrete,” or “all flow dynamics is Zeno”); and 2) we do not have a governing notion of time (t is a variable just like others).

Discussion. Efficiency or speed of verification is currently not our primary concern; still we noticed several methods for speeding up. For example, calls to the four precondition discovery strategies could be parallelized. Another performance drawback of the current prototype is that the OCaml program and *Mathematica* communicate via a file written to storage, which takes time. A tighter connection via the script mode usage of *Mathematica*, or *Mathlink*, is currently looked at.

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⁸ The source code of our precondition generator and the input `WHILEdt` programs are available at <http://www-mmm.is.s.u-tokyo.ac.jp/~ichiro/papers/vcgen.tgz>.

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A Appendix

A.1 Proofs

Proof of Lem. 2.5 The proof is much like parts of the proofs in [21]. In particular, the ‘only if’ part can be proved much like in [21, Lem. 4.5]. The proof is presented anyway for completeness.

For the ‘if’ part: assume a hyperstate σ satisfies A ; and let $(\sigma|_i)_{i \in \mathbb{N}}$ be an arbitrary sequence representation. By [21, Lem. 4.5], we have $\sigma|_i \models A|_i$ for almost every i . It follows from the assumption that $\llbracket c|_i \rrbracket(\sigma|_i) \models B|_i$. By [21, Lem. 3.10 & 4.5] this yields $\llbracket c \rrbracket \sigma \models B$.

For the ‘only if’ part: assume the negation of the right-hand side. By the definition of ultrafilter, this means:

$$\not\models \{A|_i\} c|_i \{B|_i\} \quad \text{for almost every } i,$$

which in turn is equivalent to:

for almost every i , there exists $\sigma_i \in \mathbf{St}$ such that $\sigma_i \models A|_i$ and $\llbracket c|_i \rrbracket \sigma_i \not\models B|_i$.

Pick such $\sigma_i \in \mathbf{St}$ for each such i ; for other i 's let σ_i be, say, \perp . Then let σ be the hyperstate $\sigma := [(\sigma_i)_i]$. This hyperstate σ satisfies $\sigma \models A$ and $\llbracket c \rrbracket \sigma \not\models B$, by [21, Lem. 3.10 & 4.5]. This contradicts the assumption that $\models \{A\}c\{B\}$. \square

Proof of Lem. 4.2 We follow [20] and prove the lemma using the following sublemma.⁹

Sublemma A.1 *Assume a state $\sigma \in \mathbf{St}$ is such that $\llbracket \bar{h} \rrbracket \sigma \models b_c$. Then $\llbracket h[b_c] \rrbracket \sigma = \llbracket h[\mathbf{true}] \rrbracket \sigma$.*

Proof. By induction on the construction of $h \in \mathbf{Cmd}_{\perp}$.

When $h \equiv \text{if } _ \text{ then } c_1 \text{ else } c_2$: we have $\bar{h} \equiv \text{skip}$; hence by the assumption, $\sigma \models b_c$. Therefore

$$\begin{aligned} \llbracket h[b_c] \rrbracket \sigma &= \llbracket \text{if } b_c \text{ then } c_1 \text{ else } c_2 \rrbracket \sigma \\ &= \llbracket c_1 \rrbracket \sigma && \text{by } \sigma \models b_c \\ &= \llbracket \text{if } [\mathbf{true}] \text{ then } c_1 \text{ else } c_2 \rrbracket \sigma = \llbracket h[\mathbf{true}] \rrbracket \sigma . \end{aligned}$$

When $h \equiv h'; c$, since $\bar{h} \equiv \bar{h}'$ we have $\llbracket \bar{h}' \rrbracket \sigma \models b_c$. Therefore by the induction hypothesis, we have $\llbracket h'[b_c] \rrbracket \sigma = \llbracket h'[\mathbf{true}] \rrbracket \sigma$. This is used in (*) below.

$$\llbracket h[b_c] \rrbracket \sigma = \llbracket h'[b_c]; c \rrbracket \sigma = \llbracket c \rrbracket (\llbracket h'[b_c] \rrbracket \sigma) \stackrel{(*)}{=} \llbracket c \rrbracket (\llbracket h'[\mathbf{true}] \rrbracket \sigma) = \llbracket h[\mathbf{true}] \rrbracket \sigma .$$

⁹ The statement here looks different from that of the original lemma [20, Lem. 1]. The latter holds only if the assertion language is sufficiently expressive; the statement here holds regardless.

When $h \equiv c : h'$, since $\bar{h} \equiv c; \bar{h}'$ we have $\llbracket c; \bar{h}' \rrbracket \sigma \models b_c$, that is, $\llbracket \bar{h}' \rrbracket (\llbracket c \rrbracket \sigma) \models b_c$. Therefore by the induction hypothesis, we have $\llbracket h[b_c] \rrbracket (\llbracket c \rrbracket \sigma) = \llbracket h[\text{true}] \rrbracket (\llbracket c \rrbracket \sigma)$. This is used in (*) below.

$$\llbracket h[b_c] \rrbracket \sigma = \llbracket h'[b_c] \rrbracket (\llbracket c \rrbracket \sigma) \stackrel{(*)}{=} \llbracket h'[\text{true}] \rrbracket (\llbracket c \rrbracket \sigma) = \llbracket h[\text{true}] \rrbracket \sigma .$$

When $h \equiv \text{if } b \text{ then } h' \text{ else } c$, the pre-hole fragment \bar{h} is $\bar{h} \equiv \text{assert } b; \bar{h}'$. Therefore the assumption $\llbracket \bar{h} \rrbracket \sigma \models b_c$ is equivalent to:

$$\sigma \not\models b \quad \text{or} \quad \llbracket \bar{h}' \rrbracket \sigma \models b_c . \quad (11)$$

Now assume $\sigma \not\models b$. Then

$$\begin{aligned} \llbracket h[b_c] \rrbracket \sigma &= \llbracket \text{if } b \text{ then } h'[b_c] \text{ else } c \rrbracket \sigma \\ &= \llbracket c \rrbracket \sigma && \text{by } \sigma \not\models b \\ &= \llbracket \text{if } b \text{ then } h'[\text{true}] \text{ else } c \rrbracket \sigma \\ &= \llbracket h[\text{true}] \rrbracket \sigma . \end{aligned}$$

Otherwise, by (11) we have $\llbracket \bar{h}' \rrbracket \sigma \models b_c$; by the induction hypothesis this yields $\llbracket h'[b_c] \rrbracket \sigma = \llbracket h'[\text{true}] \rrbracket \sigma$. This is used in (*) below.

$$\begin{aligned} \llbracket h[b_c] \rrbracket \sigma &= \llbracket \text{if } b \text{ then } h'[b_c] \text{ else } c \rrbracket \sigma \\ &= \llbracket h'[b_c] \rrbracket \sigma && \text{by } \sigma \models b \\ &\stackrel{(*)}{=} \llbracket h'[\text{true}] \rrbracket \sigma \\ &= \llbracket \text{if } b \text{ then } h'[\text{true}] \text{ else } c \rrbracket \sigma \\ &= \llbracket h[\text{true}] \rrbracket \sigma . \end{aligned}$$

The other case when $h \equiv \text{if } b \text{ then } c \text{ else } h'$ is similar. This concludes the proof. \square

Proof. (Of Lem. 4.2) Let us say that a sequence of states $\sigma_0, \sigma_1, \dots, \sigma_k$ is an *execution trace* of c_0 if:

- $\llbracket b_g \rrbracket \sigma_i = \text{tt}$ for each $i \in [0, k)$ and $\llbracket b_g \rrbracket \sigma_k = \text{ff}$;
- $\sigma_{i+1} = \llbracket h[b_c] \rrbracket \sigma_i$ for each $i \in [0, k)$; and
- $\sigma_i \neq \perp$ for each $i \in [0, k]$.

By the definition of denotational semantics, it is obvious that $\llbracket c_0 \rrbracket \sigma \neq \perp$ if and only if an execution trace of c_0 with $\sigma = \sigma_0$ exists; moreover, if that is the case, $\llbracket c_0 \rrbracket \sigma = \sigma_k$.

Similarly, we define an *execution trace* of c_1 to be a sequence of states $\sigma_0, \sigma_1, \dots, \sigma_k$ such that: there exists $l \in [0, k]$ and

- $\llbracket b_g \wedge \neg b \rrbracket \sigma_i = \text{tt}$ for each $i \in [0, l)$ and $\llbracket b_g \wedge \neg b \rrbracket \sigma_l = \text{ff}$;
- $\llbracket b_g \wedge b \rrbracket \sigma_i = \text{tt}$ for each $i \in [l, k)$ and $\llbracket b_g \wedge b \rrbracket \sigma_k = \text{ff}$;
- $\sigma_{i+1} = \llbracket h[\text{false}] \rrbracket \sigma_i$ for each $i \in [0, l)$;

- $\sigma_{i+1} = \llbracket h[\mathbf{true}] \rrbracket \sigma_i$ for each $i \in [l, k)$; and
- $\sigma_i \neq \perp$ for each $i \in [0, k]$.

The number l designates how many times the body of the first loop in c_1 is executed. It is again obvious that: $\llbracket c_1 \rrbracket \sigma \neq \perp$ if and only if an execution trace with $\sigma = \sigma_0$ exists; and that if that is the case $\llbracket c_1 \rrbracket \sigma = \sigma_k$.

It suffices to prove that the above two notions of execution trace (of c_0 and of c_1) coincide. The latter is an easy consequence of the following three properties, which are to be proved.

1. In an execution trace of c_0 we have:

$$\sigma_i \models b \text{ implies } \sigma_{i+1} \models b, \quad \text{for each } i \in [0, k-1).$$

2. In an execution trace of c_0 or c_1 we have:

$$\sigma_i \models b \text{ implies } \llbracket h[b_c] \rrbracket \sigma_i = \llbracket h[\mathbf{true}] \rrbracket \sigma_i, \quad \text{for each } i \in [0, k).$$

3. In an execution trace of c_0 or c_1 we have:

$$\sigma_i \not\models b \text{ implies } \llbracket h[b_c] \rrbracket \sigma_i = \llbracket h[\mathbf{false}] \rrbracket \sigma_i, \quad \text{for each } i \in [0, k).$$

To prove the property 1., assume $\sigma_i \models b_g$ with $i \in [0, k-1)$. By the definition of execution trace of c_0 we have $\sigma_i \models b$; therefore $\sigma_i \models b_g \wedge b$. By the assumption that $\models \{b_g \wedge b\} h[b_c] \{-b_g \vee b\}$ this implies

$$\llbracket h[b_c] \rrbracket \sigma_i \models \neg b_g \vee b, \quad \text{that is, } \sigma_{i+1} \models \neg b_g \vee b.$$

Now, since $i+1 \in [0, k)$, by the definition of execution trace we have $\sigma_{i+1} \models b_g$; therefore $\sigma_{i+1} \models b$.

To prove the property 2., by the assumption that $\models \{b\} \bar{h} \{b_c\}$ we have $\llbracket \bar{h} \rrbracket \sigma_i \models b_c$; thus by Lem. A.1 we have $\llbracket h[b_c] \rrbracket \sigma_i = \llbracket h[\mathbf{true}] \rrbracket \sigma_i$. The property 3. is similar. \square

Proof of Lem. 4.5 The lemma follows immediately from the following facts, which are easily proved, and the definition of denotational semantics for while-loops. First, for any command c , if a variable t does not occur in c then

$$\llbracket c \rrbracket (\sigma[t \mapsto r]) = (\llbracket c \rrbracket \sigma)[t \mapsto r],$$

for any state σ and any real number $r \in \mathbb{R}$. Second, if a Boolean expression $b \in \mathbf{BExp}$ has no occurrence of t ,

$$\llbracket b \rrbracket (\sigma[t \mapsto r]) = \llbracket b \rrbracket \sigma,$$

for any state σ and any real number $r \in \mathbb{R}$.

Proof of Lem. 5.4 $A_{\text{rs}} = A'[a'/x]^y$ means that the following holds. Note the formula is dt -free.

$$\models A_{\text{rs}}[0/y] \Leftrightarrow A' \wedge \forall u \in \mathbb{N}. (A_{\text{rs}}[u+1/y] \Leftrightarrow (A_{\text{rs}}[u/y])[a'/x]) \quad (12)$$

From this the following is trivial.

$$\models \forall d \in \mathbb{R}. (A_{\text{rs}}[0/y] \Leftrightarrow A' \wedge \forall u \in \mathbb{N}. (A_{\text{rs}}[u+1/y] \Leftrightarrow (A_{\text{rs}}[u/y])[a'/x])) \quad (13)$$

Here we appeal to the *transfer principle* (Prop. 2.7) and obtain, in ASSN^{dt} ,

$$\models \forall d \in {}^*\mathbb{R}. ({}^*A_{\text{rs}}[0/y] \Leftrightarrow {}^*A' \wedge \forall u \in {}^*\mathbb{N}. ({}^*A_{\text{rs}}[u+1/y] \Leftrightarrow ({}^*A_{\text{rs}}[u/y])[a'/x])) \quad (14)$$

Now that we are in ASSN^{dt} , we take the instance of (14) with $d = \text{dt}$ and obtain

$$\models \left(({}^*A_{\text{rs}}[\text{dt}/d])[0/y] \Leftrightarrow A \wedge \forall u \in {}^*\mathbb{N}. ({}^*A_{\text{rs}}[\text{dt}/d][u+1/y] \Leftrightarrow (({}^*A_{\text{rs}}[\text{dt}/d])[u/y])[a/x]) \right) \quad (15)$$

where we used obvious syntactic equalities ${}^*A'[\text{dt}/d] \equiv A$ and $a'[\text{dt}/d] \equiv a$. The condition (15) means that ${}^*A_{\text{rs}}[\text{dt}/d]$ is indeed a homogeneous representation (Def. 5.3). \square

Proof of Lem. 5.6 It suffices to show $\models \{a_c < 0 \wedge b\} x := a \{a_c < 0\}$; this follows immediately from the side condition. \square

Proof of Lem. 5.7 We claim that the following formula P

$$\begin{aligned} P &:= \exists z \in \mathbb{N}. Q \quad \text{where} \\ Q &:= \forall u \in \mathbb{N}. (u < z \Rightarrow b[a/x]^u) \wedge \neg b[a/x]^z \wedge A[a/x]^z \end{aligned}$$

is a loop invariant.

Firstly we prove that P is indeed a loop invariant. Assume $\sigma \models b \wedge P$. Then there exists a natural number $n_{1c} \in \mathbb{N}$ such that $\sigma \models Q[n_{1c}/z]$. If $n_{1c} = 0$ then $\sigma \models b \wedge \neg b$; this forces $\sigma = \top$ and hence trivially $\llbracket c \rrbracket \sigma \models P$.

Now assume $n_{1c} > 0$; we aim at showing $\llbracket c \rrbracket \sigma \models P$. By $\sigma \models \neg b[a/x]^{n_{1c}}$, $n_{1c} > 0$ and Cond. 2, we have $\llbracket c \rrbracket \sigma \models \neg b[a/x]^{n_{1c}-1}$. This means there exists at least one natural number $n \in \mathbb{N}$ such that $\llbracket c \rrbracket \sigma \models \neg b[a/x]^n$; take the smallest such and denote it by n'_{1c} . Obviously $n'_{1c} \leq n_{1c} - 1$; by Cond. 5 we have $\sigma \models A[a/x]^{n'_{1c}+1}$; this further yields $\llbracket c \rrbracket \sigma \models A[a/x]^{n'_{1c}}$ by Cond. 4. By the minimality requirement on n'_{1c} we have $\llbracket c \rrbracket \sigma \models \forall u \in \mathbb{N}. (u < n'_{1c} \Rightarrow b[a/x]^u)$. Now we have shown $\llbracket c \rrbracket \sigma \models Q[n'_{1c}/z]$, therefore $\llbracket c \rrbracket \sigma \models P$.

Secondly we prove that the precondition $P' := A[a/x]^{a_i+1} \wedge a_i \geq 0$ in (10) subsumes P . Assume $\sigma \models P'$. By Cond. 1 and 3 we have $\sigma \models \neg b[a/x]^{n''_{1c}}$, where $n''_{1c} := \lceil \llbracket a_i \rrbracket \sigma \rceil$ is the smallest natural number that is larger than or equal to the non-negative real number $\llbracket a_i \rrbracket \sigma$. Therefore at least for one $n \in \mathbb{N}$ we have $\sigma \models \neg b[a/x]^n$; take the smallest such and denote it by n'''_{1c} . Obviously $n'''_{1c} \leq n''_{1c} = \lceil \llbracket a_i \rrbracket \sigma \rceil$. Then by $\sigma \models P'$ and Cond. 5 we have $\sigma \models A[a/x]^{n'''_{1c}}$ and this implies $\sigma \models Q[n'''_{1c}/z]$, hence $\sigma \models P$.

Lastly we have to show $\models P \wedge \neg b \Rightarrow A$, which is obvious. \square

Proof of Lem. 5.8 It is well-known that the following are equivalent (see e.g. [9, Cor. 7.1.2]): 1) a function f is continuous at $c \in \mathbb{R}$; 2) $f(x) \simeq f(c)$ whenever $x \simeq c$. Here \simeq means that the two hyperreal numbers are *infinitely* (or *infinitesimally*) *close* to each other; and the function f in 2) is to be precise the *-transfer $*f$ of the original function [9, §3.11]. We have, for each hyperstate $\sigma \neq \perp$,

$$\llbracket a \rrbracket \sigma = \llbracket a[d/\text{dt}] \rrbracket (\sigma[d \mapsto \text{dt}]) \stackrel{(*)}{\simeq} \llbracket a[d/\text{dt}] \rrbracket (\sigma[d \mapsto 0]) = \llbracket (a[d/\text{dt}])[0/d] \rrbracket \sigma, \quad (16)$$

where $(*)$ holds from the continuity assumption (Cond. 2.) and that $\text{dt} \simeq 0$.

The expression $(a[d/\text{dt}])[0/d]$ is dt -free; thus by Cond. 1., the value $\llbracket (a[d/\text{dt}])[0/d] \rrbracket \sigma$ is in fact a standard real number. It is straightforward that, given a standard real number r and a hyperreal number r' such that $r \simeq r'$, we have $r < 0$ imply $r' < 0$. Thus (16) yields the claim. \square

A.2 The Full Iteration Count Strategy

Lemma A.2 (Full iteration count) *Let a_{ii} and a_{io} be arithmetic expressions. Assume the following conditions.*

1. $\models b_o([f/y]^{a_{ii}}[e/x])^{a_{io}}$
2. $\models \forall u, u', v, v', w, w' \in \mathbb{R}. (0 \leq u' < u \wedge 0 \leq v' < v \wedge 0 \leq w' < w \wedge A([f/y]^u[e/x]^v[f/y]^w) \Rightarrow A([f/y]^{u'}[e/x]^{v'}[f/y]^{w'}))$
3. $\models \forall u, u', v, v' \in \mathbb{R}. (0 \leq u < u' \wedge 0 \leq v < v' \wedge \neg b_o([f/y]^v[e/x]^u) \Rightarrow \neg b_o([f/y]^{v'}[e/x]^{u'}))$
4. $\models \neg b_i[f/y]^{a_{ii}}$
5. $\models \forall u, v \in \mathbb{R}. (u < v \wedge \neg b_i[f/y]^u \Rightarrow \neg b_i[f/y]^v)$
6. $\models \forall u \in \mathbb{R}. (0 \leq u < a_{ii} \Rightarrow b_i[f/y]^u)$

Then we have

$$\left\{ A([f/y]^{a_{ii}+1}[e/x])^{a_{io}+1} \wedge a_{ii} \geq 0 \wedge a_{io} \geq 0 \right\} c_0 \{A\}, \quad (17)$$

where $c_0 \quad ::= \quad \text{while } b_o \text{ do } (x := e; \quad \text{while } b_i \text{ do } y := f) .$

Here expressions like $A([f/y]^v[e/x])^u$ are the obvious generalization of Def. 5.1.

In the proof we will use two lemmas: one is Lem. 5.7 and the other is Sublem. A.3 below. Each of them is about estimating the number of iterations for a single loop.

The operator $\lceil _ \rceil$ occurs in the proofs. We note that its use is confined to the backend and it does not occur in Lem. A.2. Therefore in the actual verification using Lem. A.2, we do not have to deal with the discontinuous (hence inconvenient) function $\lceil _ \rceil$.

Sublemma A.3 *Let a_i be an arithmetic expression. Assume the following conditions.*

1. $\models \neg b[a/x]^{a_i}$
2. $\models \forall u \in \mathbb{N}. (0 \leq u < a_i \Rightarrow b[a/x]^{a_i})$
3. *For some fresh variable y , $\models \{b[a/x]^{y+1}\} c \{b[a/x]^y\}$*
4. *For some fresh variable y , $\models \{\neg b[a/x]^{y+1}\} c \{\neg b[a/x]^y\}$*
5. $\models \forall u, v \in \mathbb{R}. (a_i \leq u < v < a_i + 1 \wedge \neg b[a/x]^u \Rightarrow \neg b[a/x]^v)$

6. For some fresh variable y , $\models \{A[a/x]^{y+1}\} c \{A[a/x]^y\}$

Then we have

$$\models \{A[a/x]^{\lceil a_i \rceil} \wedge a_i \geq 0\} \text{ while } b \text{ do } c \{A\} . \quad (18)$$

Proof. The proof goes much like the one for Lem. 5.7, with the same invariant P . Cond. 1–4 forces a_i to be a good estimate of the number of iterations; in fact we can prove the latter to be precisely $\lceil a_i \rceil$. \square

Proof. (Of Lem. A.2) It suffices to prove

$$\models A([f/y]^{a_{ii}+1}[e/x])^{a_{io}+1} \Rightarrow A([f/y]^{\lceil a_{ii} \rceil}[e/x])^{a_{io}+1} \quad \text{and} \quad (19)$$

$$\models \left\{ A([f/y]^{\lceil a_{ii} \rceil}[e/x])^{a_{io}+1} \wedge a_{ii} \geq 0 \wedge a_{io} \geq 0 \right\} c_0 \{A\} . \quad (20)$$

Cond. 2 immediately yields (19). The remaining subgoal (20) is proved using Lem. 5.7; its side conditions are new proof obligations. Among them, the following two do not follow immediately from the assumptions of Lem. A.2:

$$\models \left\{ A([f/y]^{\lceil a_{ii} \rceil}[e/x])^{y+1} \wedge a_{ii} \geq 0 \right\} x := e; \text{ while } b_i \text{ do } y := f \quad (21)$$

$$\{A([f/y]^{\lceil a_{ii} \rceil}[e/x])^y\} ,$$

$$\models \left\{ \neg b_o([f/y]^{\lceil a_{ii} \rceil}[e/x])^{y+1} \wedge a_{ii} \geq 0 \right\} x := e; \text{ while } b_i \text{ do } y := f \quad (22)$$

$$\{\neg b_o([f/y]^{\lceil a_{ii} \rceil}[e/x])^y\} .$$

The obligation (21) is discharged via Lem. 5.7; the obligation (22) is via Lem. A.3. \square