

[1] KS and IH. **Programming with Infinitesimals: A While-Language for Hybrid System Modeling**. Proc. ICALP 2011, Track B.
 [2] IH and KS. **Exercises in Nonstandard Static Analysis of Hybrid Systems**. Proc. CAV 2012.

Nonstandard Static Analysis

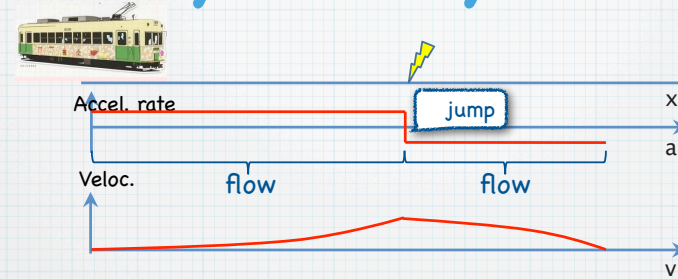
Transfer Verification to Hybrid Systems

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Hybrid System



- * Flow & jump
- * Digital control in a physical environment
- * Component of cyber-physical systems

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Hybrid System

Formal verification
(computer science)

Discrete
"jump"

and

Continuous
"flow"

Control theory
(applied analysis)

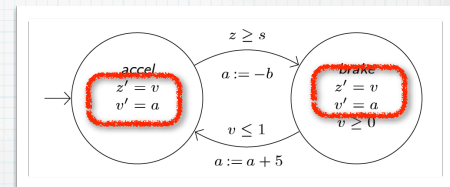


- Flow?
- With minimal cost?

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Formal Verification Approaches

- * Hybrid automata
[Alur, Henzinger, ...; '90s-]



- * Differential dynamic logic
[Platzer & others, '07-]

$$[\dot{x} = 1 \text{ while } x \leq 3] \phi$$

- * Differential equations, explicitly
→ distinction jump vs. flow

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"Turn Flow into Jump"

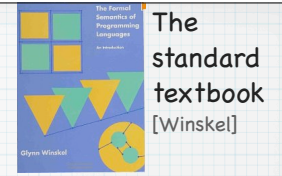
```
t := 0 ;
while (t ≤ 1) do {
  t := t + dt
}
```

- * Infinitesimal number dt
- * "Infinitely small" : $0 < dt < r$ for any positive real r
- * $t = 1$ after the execution?
- * Non-standard analysis! [Robinson '60s]

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Theoretical Framework

[Suenaga&H., ICALP'11]



While ^{dt}	Assn ^{dt}	Hoare ^{dt}
Programming lang.	First-order assertion lang.	Hoare-style program logic
while (t<a) do { t:=t+1; if ... }	$\exists z(x=2*z \wedge y=3*z)$	$\frac{\{A \wedge b\} c \{A\}}{\{A\} \text{while } b \text{ do } c \{A \wedge \neg b\}}$

Rigorous semantics by non-standard analysis

- Hoare^{dt} : sound and relatively complete
- Program verification/static analysis of hybrid systems
- Actual verification with NSA

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Program Verif. Techniques
* Esp. invariant discovery

Static Analysis

Nonstandard Static Analysis

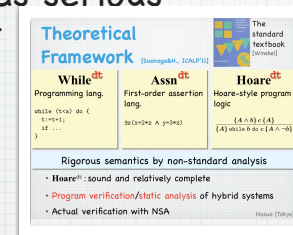
Nonstandard Analysis

Infinitesimal dt

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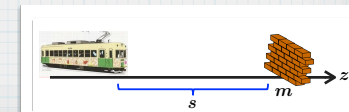
Nonstandard Static Analysis

- * Towards serious use of

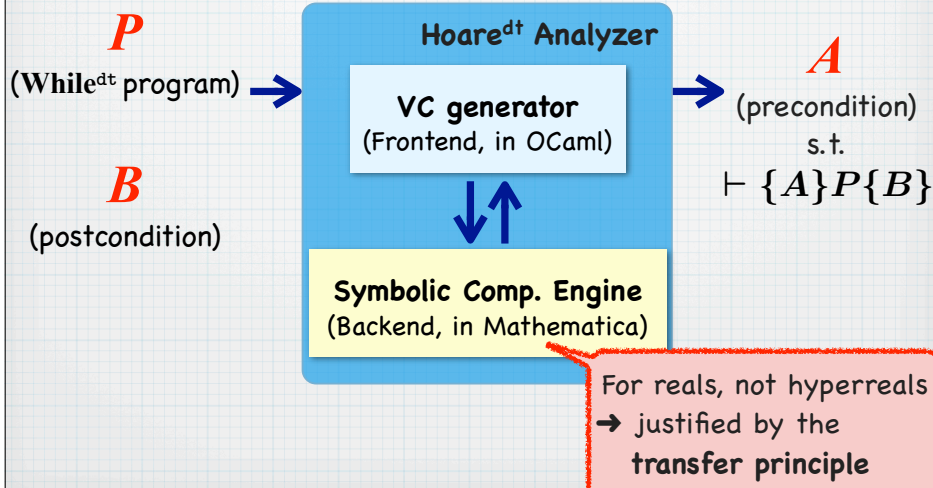


Exactly as they are!

- * Static analysis techniques transferred to hybrid appl.
- * Leading example: ETCS

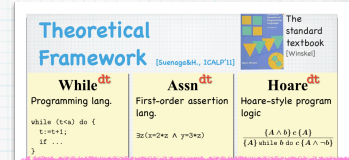


Prototype Automatic Prover



Outline

- * Theoretical foundations
 - * While^{dt}, Assn^{dt}, Hoare^{dt}
 - * Rigorous semantics via NSA
 - * Transfer principle, "sectionwise lemmas"
- * Static analysis techniques, transferred as they are
 - * Phase split [Sharma,Dillig,Dillig,Aiken; CAV'11]
[Balakrishnan,Sankaranarayanan,Ivancic,Gupta; EMSOFT'09] [Gopan,Reps; SAS'07]
 - * Differential invariant [Platzer,Clarke; CAV'08]
 - * ... and more!



w/ or w/o dt ...
 → logically "the same"

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Part I: Theoretical Foundations

Nonstandard Analysis

- * Analysis with an infinitesimal δ , e.g. "Infinitely small"
 $0 < \delta < r$
 $(\forall r \in \mathbb{R}_+)$
- * Cf. Leibniz's monad
- * Done naively → contradiction!

$$f \text{ is continuous} \iff \left(\begin{array}{l} |x - x'| \text{ is infinitesimal} \\ \implies |f(x) - f(x')| \text{ is infinitesimal} \end{array} \right)$$



Logical foundation via an ultrafilter

[Robinson,1960]

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Hyperreals

= Reals + Infinitesimals + ...

Defn.

The set of hyperreal numbers is

$${}^*\mathbb{R} := \mathbb{R}^{\mathbb{N}} / \sim_{\mathcal{F}} \ni [(a_0, a_1, a_2, \dots)]$$

Ignore

* Operations:
sectionwise

$$+ \begin{bmatrix} (a_0, a_1, \dots) \\ (b_0, b_1, \dots) \\ (a_0 + b_0, a_1 + b_1, \dots) \end{bmatrix}$$

* Reals are hyperreals

$$\mathbb{R} \hookrightarrow {}^*\mathbb{R}, \\ r \mapsto [(r, r, \dots)]$$

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0th section
1st section
2nd section

Hyperreals

= Reals + Infinitesimals + ...

Defn.

The set of hyperreal numbers is

$${}^*\mathbb{R} := \mathbb{R}^{\mathbb{N}} / \sim_{\mathcal{F}} \ni [(a_0, a_1, a_2, \dots)]$$

* Predicates:
sectionwise,
"for almost all i "

$$[(a_i)_{i \in \mathbb{N}}] < [(b_i)_{i \in \mathbb{N}}] \\ \iff a_i < b_i \quad \text{"for almost every } i\text{"} \\ \iff \{i \in \mathbb{N} \mid a_i \not< b_i\} \text{ is finite}$$

"For sufficiently large i "
"Except for finitely many i "

Precise defn. is via an ultrafilter \mathcal{F} :

$$[(a_i)_{i \in \mathbb{N}}] < [(b_i)_{i \in \mathbb{N}}] \\ \iff \{i \in \mathbb{N} \mid a_i < b_i\} \in \mathcal{F}$$

Hyperreals

= Reals + Infinitesimals + ...

Defn.

The set of hyperreal numbers is

$${}^*\mathbb{R} := \mathbb{R}^{\mathbb{N}} / \sim_{\mathcal{F}}$$

$$[(a_i)_{i \in \mathbb{N}}] < [(b_i)_{i \in \mathbb{N}}] \\ \iff a_i < b_i \quad \text{"for almost every } i\text{"} \\ \iff \{i \in \mathbb{N} \mid a_i \not< b_i\} \text{ is finite}$$

Prop. $\omega^{-1} = [(1, \frac{1}{2}, \frac{1}{3}, \dots)]$ is infinitesimal.

$$\omega^{-1} = (1, \frac{1}{2}, \frac{1}{3}, \dots, \frac{1}{N}, \frac{1}{N+1}, \dots)$$

OK! \wedge \times \times \times \times \wedge \wedge ...

$$\frac{1}{N} = (\frac{1}{N}, \frac{1}{N}, \frac{1}{N}, \dots, \frac{1}{N}, \frac{1}{N}, \dots)$$

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Hyperreals

= Reals + Infinitesimals + ...

Defn.

The set of hyperreal numbers is

$${}^*\mathbb{R} := \mathbb{R}^{\mathbb{N}} / \sim_{\mathcal{F}}$$

$$[(a_i)_{i \in \mathbb{N}}] < [(b_i)_{i \in \mathbb{N}}] \\ \iff a_i < b_i \quad \text{"for almost every } i\text{"} \\ \iff \{i \in \mathbb{N} \mid a_i \not< b_i\} \text{ is finite}$$

Prop. $\omega^{-1} = [(1, \frac{1}{2}, \frac{1}{3}, \dots)]$ is infinitesimal.

Prop. $\omega = [(1, 2, 3, \dots)]$ is infinite.

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Hype

= Reals + Inf

Ultrafilter (existence by AC)

Defn. An ultrafilter $\mathcal{F} \subseteq \mathcal{P}(\mathbb{N})$ is such that:

- For each $X \subseteq \mathbb{N}$, exactly one of X and $\mathbb{N} \setminus X$ is in \mathcal{F} .
- $X, Y \in \mathcal{F} \Rightarrow X \cap Y \in \mathcal{F}$
- $X \in \mathcal{F}, X \subseteq Y \Rightarrow Y \in \mathcal{F}$
- $\emptyset \notin \mathcal{F}$

Defn. The set of hyperreal numbers is

$${}^*\mathbb{R} := \mathbb{R}^{\mathbb{N}} / \sim_{\mathcal{F}}$$

$$[(a_i)_{i \in \mathbb{N}}] < [(b_i)_{i \in \mathbb{N}}] \iff \{i \in \mathbb{N} \mid a_i < b_i\} \in \mathcal{F}$$

Thm. (Transfer Principle)
 A : a first-order formula.
 $*A$: its $*$ -transform. Then


$$\mathbb{R} \models A \iff {}^*\mathbb{R} \models *A.$$

Same as A , except:
 $\forall x \in \mathbb{R}$ in A is
 $\forall x \in {}^*\mathbb{R}$ in $*A$

\mathbb{R} and $*\mathbb{R}$ are
 "logically the same"

Theoretical Framework

[Suenaga&H., ICALP'11]



The standard textbook [Winskel]

While^{dt}
Programming lang.

```
while (t < a) do {
  t := t + 1;
  if ...
}
```


Assn^{dt}
First-order assertion lang.

$$\exists z (x = 2 * z \wedge y = 3 * z)$$

Hoare^{dt}
Hoare-style program logic

$$\frac{\{A \wedge b\} c \{A\}}{\{A\} \text{ while } b \text{ do } c \{A \wedge \neg b\}}$$

Rigorous semantics by non-standard analysis



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Syntax

While^{dt} While + dt

AExp $\ni a ::= x \mid c_r \mid a_1 \text{ aop } a_2$ dt
 where c_r is a const. for $r \in \mathbb{R}$, aop $\in \{+, -, \cdot, ^, /\}$

BExp $\ni b ::= \text{true} \mid \text{false} \mid b_1 \wedge b_2 \mid \neg b \mid a_1 < a_2$

Cmd $\ni c ::= \text{skip} \mid x := a \mid c_1; c_2 \mid \text{if } b \text{ then } c_1 \text{ else } c_2 \mid \text{while } b \text{ do } c$

Assn^{dt}

$A ::= \text{true} \mid \text{false} \mid A_1 \wedge A_2 \mid \neg A \mid a_1 < a_2 \mid \forall x \in {}^*\mathbb{N}. A \mid \forall x \in {}^*\mathbb{R}. A$

Hoare^{dt}

$\frac{}{\{A\} \text{ skip } \{A\}}$ (SKIP)	$\frac{}{\{A[a/x]\} x := a \{A\}}$ (ASSIGN)
$\frac{\{A\} c_1 \{C\} \quad \{C\} c_2 \{B\}}{\{A\} c_1; c_2 \{B\}}$ (SEQ)	$\frac{\{A \wedge b\} c_1 \{B\} \quad \{A \wedge \neg b\} c_2 \{B\}}{\{A\} \text{ if } b \text{ then } c_1 \text{ else } c_2 \{B\}}$ (IF)
$\frac{\{A \wedge b\} c \{A\}}{\{A\} \text{ while } b \text{ do } c \{A \wedge \neg b\}}$ (WHILE)	$\frac{\models A \Rightarrow A' \quad \{A'\} c \{B'\} \quad \models B' \Rightarrow B}{\{A\} c \{B\}}$ (CONSEQ)

Tokyo

Syntax

While^{dt} While + dt

AExp $\ni a ::= x \mid c_r \mid a_1 \text{ aop } a_2$ dt
 where c_r is a const. for $r \in \mathbb{R}$, aop $\in \{+, -, \cdot, ^, /\}$

BExp $\ni b ::= \text{true} \mid \text{false} \mid b_1 \wedge b_2 \mid \neg b \mid a_1 < a_2$

Cmd $\ni c ::= \text{skip} \mid x := a \mid c_1; c_2 \mid \text{if } b \text{ then } c_1 \text{ else } c_2 \mid \text{while } b \text{ do } c$

Assn^{dt} Assn, *-transformed

$A ::= \text{true} \mid \text{false} \mid A_1 \wedge A_2 \mid \neg A \mid a_1 < a_2 \mid \forall x \in {}^*\mathbb{N}. A \mid \forall x \in {}^*\mathbb{R}. A$

Hoare^{dt}

$\frac{}{\{A\} \text{ skip } \{A\}}$ (SKIP)	$\frac{}{\{A[a/x]\} x := a \{A\}}$ (ASSIGN)
$\frac{\{A\} c_1 \{C\} \quad \{C\} c_2 \{B\}}{\{A\} c_1; c_2 \{B\}}$ (SEQ)	$\frac{\{A \wedge b\} c_1 \{B\} \quad \{A \wedge \neg b\} c_2 \{B\}}{\{A\} \text{ if } b \text{ then } c_1 \text{ else } c_2 \{B\}}$ (IF)
$\frac{\{A \wedge b\} c \{A\}}{\{A\} \text{ while } b \text{ do } c \{A \wedge \neg b\}}$ (WHILE)	$\frac{\models A \Rightarrow A' \quad \{A'\} c \{B'\} \quad \models B' \Rightarrow B}{\{A\} c \{B\}}$ (CONSEQ)

Tokyo

Syntax

While^{dt}

While + dt

$AExp \ni a ::= x \mid c_r \mid a_1 \text{ aop } a_2 \mid dt$
 where c_r is a const. for $r \in \mathbb{R}$, aop $\in \{+, -, \cdot, ^, /\}$

$BExp \ni b ::= \text{true} \mid \text{false} \mid b_1 \wedge b_2 \mid \neg b \mid a_1 < a_2$
 $c ::= \text{skip} \mid x := a \mid c_1; c_2$
 $\quad \mid \text{if } b \text{ then } c_1 \text{ else } c_2 \mid \text{while } b \text{ do } c$

Thm.
 $HOARE^{dt}$ rules are *sound* and *relatively complete*.

Hoare^{dt}

Precisely the same

$\frac{}{\{A\} \text{skip } \{A\}}$ (SKIP)	$\frac{}{\{A[a/x]\} x := a \{A\}}$ (ASSIGN)
$\frac{\{A\} c_1 \{C\} \quad \{C\} c_2 \{B\}}{\{A\} c_1; c_2 \{B\}}$ (SEQ)	$\frac{\{A \wedge b\} c_1 \{B\} \quad \{A \wedge \neg b\} c_2 \{B\}}{\{A\} \text{if } b \text{ then } c_1 \text{ else } c_2 \{B\}}$ (IF)
$\frac{\{A \wedge b\} c \{A\}}{\{A\} \text{while } b \text{ do } c \{A \wedge \neg b\}}$ (WHILE)	$\frac{\models A \Rightarrow A' \quad \{A'\} c \{B'\} \quad \models B' \Rightarrow B}{\{A\} c \{B\}}$ (CONSEQ)

Tokyo

Denotational Semantics: Challenge

```
t := 0 ;
while (t ≤ 1) do {
  t := t + dt
}
```

```
t := 0 ;
while (true) do {
  t := t + dt
}
```

$t = 1 + dt$

\perp (divergence)

* Semantics by "sectionwise execution"

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Denotational Semantics

* Execute sectionwise and bundle up the outcomes!

```
t := 0;
while (t < 1)
  t := t + dt;
```

Hasuo (Tokyo)

Denotational Semantics

* Execute sectionwise and bundle up the outcomes!

```
t := 0;
while (t < 1)
  t := t + dt;
```

Hasuo (Tokyo)

Denotational Semantics

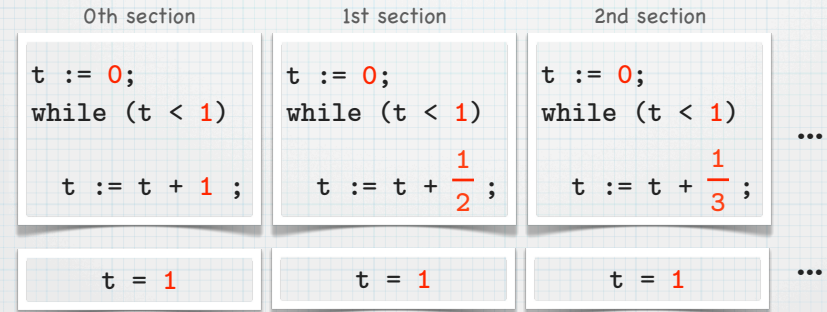
- * Execute sectionwise and bundle up the outcomes!

```
t := (0,0,0,...);  
while (t < (1,1,1,...))  
  t := t + (1, 1/2, 1/3, ...);
```

Hasuo (Tokyo)

Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!



Hasuo (Tokyo)

Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!

```
t := (0,0,0,...);  
while (t < (1,1,1,...))  
  t := t + (1, 1/2, 1/3, ...);
```

```
t = (1,1,1,...)
```

Hasuo (Tokyo)

Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!

```
t := 0;  
while (t < 1)  
  t := t + dt;
```

```
t = 1
```

Hasuo (Tokyo)

Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!

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Hasuo (Tokyo)

Denotational Semantics

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Hasuo (Tokyo)

Denotational Semantics

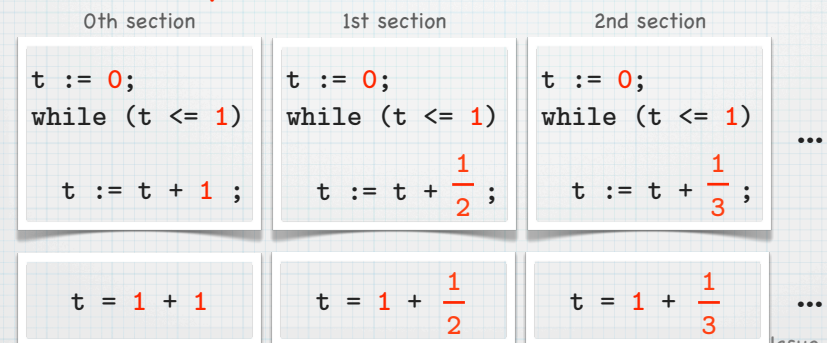
- * Execute sectionwise and bundle up the outcomes!

```
t := (0,0,0,...);  
while (t <= (1,1,1,...))  
  t := t + (1,  $\frac{1}{2}$ ,  $\frac{1}{3}$ , ...);
```

Hasuo (Tokyo)

Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!



Hasuo (Tokyo)

Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!

```
t := (0,0,0,...);  
while (t <= (1,1,1,...))  
  t := t + (1,  $\frac{1}{2}$ ,  $\frac{1}{3}$ , ...);
```

```
t = (1,1,1,...) + (1,  $\frac{1}{2}$ ,  $\frac{1}{3}$ , ...)
```

Hasuo (Tokyo)

Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!

```
t := 0;  
while (t <= 1)  
  t := t + dt;
```

```
t = 1 + dt
```

Hasuo (Tokyo)

Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!

```
t := 0;  
while (true)  
  t := t + dt;
```

Hasuo (Tokyo)

Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!

```
t := 0;  
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Hasuo (Tokyo)

Denotational Semantics

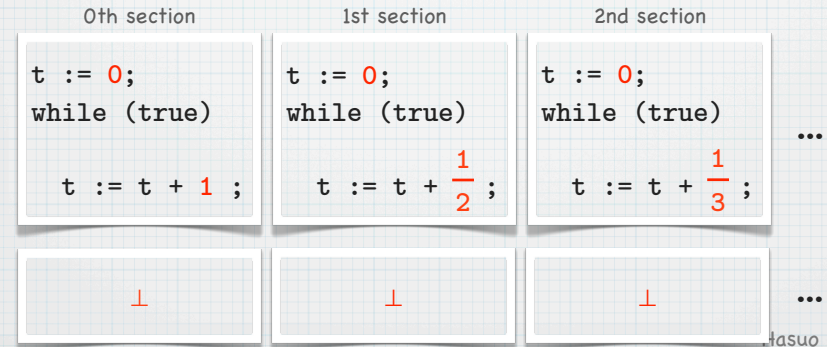
- * Execute sectionwise and bundle up the outcomes!

```
t := (0,0,0,...);  
while (true)  
  t := t + (1,  $\frac{1}{2}$ ,  $\frac{1}{3}$ , ...);
```

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Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!



Hasuo (Tokyo)

Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!

```
t := (0,0,0,...);  
while (true)  
  t := t + (1,  $\frac{1}{2}$ ,  $\frac{1}{3}$ , ...);
```

```
t = (⊥, ⊥, ⊥, ...)
```

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Denotational Semantics

- * Execute sectionwise and bundle up the outcomes!

```
t := 0;  
while (true)  
  t := t + dt;
```

```
⊥
```

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Denot

$$\left[\begin{array}{l} t := 0; \\ \text{while } (t \leq 1) \text{ do} \\ t := t + dt \end{array} \right] \xrightarrow{i\text{-th section}} \left[\begin{array}{l} t := 0; \\ \text{while } (t \leq 1) \text{ do} \\ t := t + \frac{1}{i+1} \end{array} \right]$$

Hyperstate (stores hyperreals)

$\llbracket x \rrbracket \sigma := \sigma(x)$
 $\llbracket a_1 \text{ aop } a_2 \rrbracket \sigma := \llbracket a_1 \rrbracket \sigma \text{ aop } \llbracket a_2 \rrbracket \sigma$
 $\llbracket dt \rrbracket \sigma := \omega^{-1} = [(1, \frac{1}{2}, \frac{1}{3}, \dots)]$

$\llbracket \text{true} \rrbracket \sigma := \text{tt}$
 $\llbracket b_1 \wedge b_2 \rrbracket \sigma := \llbracket b_1 \rrbracket \sigma \wedge \llbracket b_2 \rrbracket \sigma$
 $\llbracket a_1 < a_2 \rrbracket \sigma := \llbracket a_1 \rrbracket \sigma < \llbracket a_2 \rrbracket \sigma$

Def.

The *i*-th section of a WHILE^{dt} expression *e* is

$$e|_i \equiv e \left[\frac{1}{i+1} / dt \right].$$

$\llbracket \text{skip} \rrbracket \sigma := \sigma$ $\llbracket x := a \rrbracket \sigma := \sigma[x \mapsto \llbracket a \rrbracket \sigma]$ $\llbracket c_1; c_2 \rrbracket \sigma := \llbracket c_2 \rrbracket (\llbracket c_1 \rrbracket \sigma)$

$\llbracket \text{if } b \text{ then } c_1 \text{ else } c_2 \rrbracket \sigma := \begin{cases} \llbracket c_1 \rrbracket \sigma & \text{if } \llbracket b \rrbracket \sigma = \text{tt} \\ \llbracket c_2 \rrbracket \sigma & \text{if } \llbracket b \rrbracket \sigma = \text{ff} \end{cases}$

$\llbracket \text{while } b \text{ do } c \rrbracket \sigma := \left(\llbracket (\text{while } b \text{ do } c)|_i \rrbracket (\sigma|_i) \right)_{i \in \mathbb{N}}$

Bundled up

Section of a program

Applied to a section of a memory state

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"Sectionwise Lemmas"

Sectionwise Execution Lemma.

For any expr. *e* and *i* ∈ ℕ,

$$\llbracket e \rrbracket \sigma = \left[\left(\llbracket e|_i \rrbracket (\sigma|_i) \right)_{i \in \mathbb{N}} \right].$$

Sectionwise Satisfaction Lemma.

For any hyperstate σ and an ASSN^{dt} formula φ :

$$\sigma \models \varphi \iff \sigma|_i \models \varphi|_i \text{ for almost every } i.$$

£os'

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"Sectionwise Lemmas"

Lem. (Sectionwise validity of Hoare triples)

$$\models \{A\}c\{B\} \iff \models \{A|_i\}c|_i\{B|_i\} \text{ for almost every } i.$$

Interface for transferring static analysis techniques

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Q. Is a While^{dt} program executable?

- * A. Not exactly.
- * A modeling language
 - * Not numerical approx., but exact modeling
 - * Advantage: close to a common programming style
- * Static analysis → no need to execute!
- * Mathematical semantics suffices

Hasuo (Tokyo)

Outline

Suenaga & H.,
ICALP'11

* Theoretical foundations

- * While^{dt} , Assn^{dt} , Hoare^{dt}
- * Rigorous semantics via NSA
- * Transfer principle, "sectionwise lemmas"

w/ or w/o dt ...
→ logically "the same"

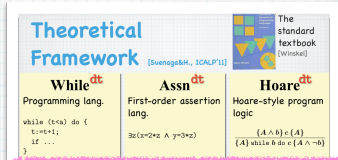
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H. & Suenaga,
CAV'12

* Static analysis techniques, transferred as they are

- * Phase split [Sharma,Dillig,Dillig,Aiken; CAV'11]
[Balakrishnan,Sankaranarayanan,Ivancic,Gupta; EMSOFT'09] [Gopan,Reps; SAS'07]
- * Differential invariant [Platzer,Clarke; CAV'08]
- * ... and more!

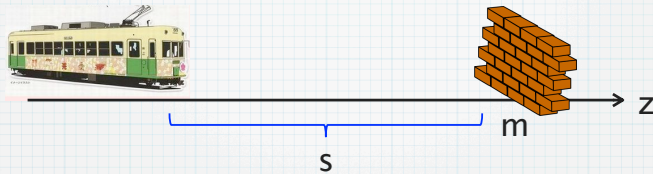
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Part II: Exercises in Nonstandard Static Analysis

Exercise 1.1

(Tiny) fragment of
Euro. Train Ctrl. Sys. (ETCS)



while $t < \epsilon$ do {
 ;
};

s : big enough
 b : big enough
 a_0 : small enough
...

```
while v > 0 do {
  t := 0;
  if m - z < s then a := -b else a := a0;
  while t < ε do {
    t := t + dt;
    v := v + a * dt;
    z := z + v * dt;
  }
}
```

ETCS₀

Q. Find A s.t. $\models \{A\} \text{ETCS}_0 \{z < m\}$

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```
while (v > 0) {
  if m - z < s
  then a := -b
  else a := a0;
  t := 0;
  while (t < eps && v > 0) {
    z := z + v * dt;
    v := v + a * dt;
    t := t + dt;
  }
}
```

{z < m}

```
while (v > 0 && m - z >= s) {
  a := a0; t := 0;
  while (t < eps && v > 0) {
    z := z + v * dt;
    v := v + a0 * dt;
    t := t + dt;
  };
  while (v > 0 && m - z < s) {
    a := -b; t := 0;
    while (t < eps && v > 0) {
      z := z + v * dt;
      v := v - b * dt;
      t := t + dt;
    }
  }
}
```

accel.

brake

{z < m}

Strategy1 "Phase split"

[Sharma,Dillig,Dillig,Aiken; CAV'11]
[Balakrishnan,Sankaranarayanan,Ivancic,Gupta; EMSOFT'09] [Gopan,Reps; SAS'07]

Phase Split (Standard Ver., for While & Hoare)

Defn.
The set of *holed commands* Cmd_{\square} is:

$$\text{Cmd}_{\square} \ni h ::= \text{if } \square \text{ then } c_1 \text{ else } c_2 \mid h; c \mid c; h \mid \text{if } b \text{ then } h \text{ else } c \mid \text{if } b \text{ then } c \text{ else } h$$

For each holed command h , its *pre-hole fragment* \bar{h} is:

$$\begin{aligned} \overline{\text{if } \square \text{ then } c_1 \text{ else } c_2} &::= \text{skip} \\ \overline{h; c} &::= \bar{h} \quad \overline{c; h} ::= c; \bar{h} \\ \overline{\text{if } b \text{ then } h \text{ else } c} &::= \text{assert } b; \bar{h} \\ \overline{\text{if } b \text{ then } c \text{ else } h} &::= \text{assert } \neg b; \bar{h} \end{aligned}$$

$$\text{while } b_g \text{ do } \dots (\text{if } \dots) \dots$$

$$\text{into } \left[\begin{array}{l} \text{while } b_g \wedge \neg b_s \text{ do } \dots; \\ \text{while } b_g \wedge b_s \text{ do } \dots \end{array} \right]$$

Lem.
If a Boolean expression $b_s \in \text{BExp}$ satisfies

$$\models \{b_s\} \bar{h} \{b_c\}, \quad \models \{\neg b_s\} \bar{h} \{\neg b_c\}, \quad \text{and} \quad \models \{b_g \wedge b_s\} h \{b_c\} \{\neg b_g \vee b_s\},$$

then we have

$$\llbracket \text{while } b_g \text{ do } h \{b_c\} \rrbracket = \llbracket \begin{array}{l} \text{while } (b_g \wedge \neg b_s) \text{ do } h[\text{false}]; \\ \text{while } (b_g \wedge b_s) \text{ do } h[\text{true}] \end{array} \rrbracket.$$

$h[_]$ is a command containing
if [_] then ... else ...

Hasuo (Tokyo)

Phase Split (Nonstandard Ver., for While^{dt} & Hoare^{dt})

Defn.
The set of *holed commands* Cmd_{\square} is:

$$\text{Cmd}_{\square} \ni h ::= \text{if } \square \text{ then } c_1 \text{ else } c_2 \mid h; c \mid c; h \mid \text{if } b \text{ then } h \text{ else } c \mid \text{if } b \text{ then } c \text{ else } h$$

For each holed command h , its *pre-hole fragment* \bar{h} is:

$$\begin{aligned} \overline{\text{if } \square \text{ then } c_1 \text{ else } c_2} &::= \text{skip} \\ \overline{h; c} &::= \bar{h} \quad \overline{c; h} ::= c; \bar{h} \\ \overline{\text{if } b \text{ then } h \text{ else } c} &::= \text{assert } b; \bar{h} \\ \overline{\text{if } b \text{ then } c \text{ else } h} &::= \text{assert } \neg b; \bar{h} \end{aligned}$$

Lem.
If a Boolean expression $b_s \in \text{BExp}$ satisfies

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$$\llbracket \text{while } b_g \text{ do } h \{b_c\} \rrbracket = \llbracket \begin{array}{l} \text{while } (b_g \wedge \neg b_s) \text{ do } h[\text{false}]; \\ \text{while } (b_g \wedge b_s) \text{ do } h[\text{true}] \end{array} \rrbracket.$$

Proof.

$\models \{b_s\} \bar{h} \{b_c\}$
 $\models \{\neg b_s\} \bar{h} \{\neg b_c\}$
 $\models \{b_g \wedge b_s\} h \{b_c\} \{\neg b_g \vee b_s\}$

\Leftrightarrow sectionwise

$\models \{b_s\} \bar{h} \{b_c\}$
 $\models \{\neg b_s\} \bar{h} \{\neg b_c\}$
 $\models \{b_g \wedge b_s\} h \{b_c\} \{\neg b_g \vee b_s\}$

\vdots

$\models \{b_s|_i\} \bar{h}|_i \{b_c|_i\}$
 $\models \{\neg b_s|_i\} \bar{h}|_i \{\neg b_c|_i\}$
 $\models \{b_g|_i \wedge b_s|_i\} h|_i \{b_c|_i\} \{\neg b_g|_i \vee b_s|_i\}$

\vdots

$\models \{b_s|_i\} \bar{h}|_i \{b_c|_i\}$
 $\models \{\neg b_s|_i\} \bar{h}|_i \{\neg b_c|_i\}$
 $\models \{b_g|_i \wedge b_s|_i\} h|_i \{b_c|_i\} \{\neg b_g|_i \vee b_s|_i\}$

\Rightarrow std. ver.

$\llbracket \text{while } b_g \text{ do } h \{b_c\} \rrbracket$
 $= \llbracket \begin{array}{l} \text{while } (b_g \wedge \neg b_s) \text{ do } h[\text{false}]; \\ \text{while } (b_g \wedge b_s) \text{ do } h[\text{true}] \end{array} \rrbracket$

(for almost all i)

Hasuo (Tokyo)

Transferring Static Analysis Strategies

$\models \{b_s\} \bar{h} \{b_c\}$
 $\models \{\neg b_s\} \bar{h} \{\neg b_c\}$
 $\models \{b_g \wedge b_s\} h \{b_c\} \{\neg b_g \vee b_s\}$

\Leftrightarrow sectionwise

$\models \{b_s\} \bar{h} \{b_c\}$
 $\models \{\neg b_s\} \bar{h} \{\neg b_c\}$
 $\models \{b_g \wedge b_s\} h \{b_c\} \{\neg b_g \vee b_s\}$

\vdots

$\models \{b_s|_i\} \bar{h}|_i \{b_c|_i\}$
 $\models \{\neg b_s|_i\} \bar{h}|_i \{\neg b_c|_i\}$
 $\models \{b_g|_i \wedge b_s|_i\} h|_i \{b_c|_i\} \{\neg b_g|_i \vee b_s|_i\}$

\vdots

$\models \{b_s|_i\} \bar{h}|_i \{b_c|_i\}$
 $\models \{\neg b_s|_i\} \bar{h}|_i \{\neg b_c|_i\}$
 $\models \{b_g|_i \wedge b_s|_i\} h|_i \{b_c|_i\} \{\neg b_g|_i \vee b_s|_i\}$

\Rightarrow std. ver.

$\llbracket \text{while } b_g \text{ do } h \{b_c\} \rrbracket$
 $= \llbracket \begin{array}{l} \text{while } (b_g \wedge \neg b_s) \text{ do } h[\text{false}]; \\ \text{while } (b_g \wedge b_s) \text{ do } h[\text{true}] \end{array} \rrbracket$

(for almost all i)

- * Doesn't matter what "std. ver." is
- * \rightarrow **modular method** for transfer

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```
while (v > 0) {
  if m - z < s
  then a := -b
  else a := a0;
  t := 0;
  while (t < eps && v > 0) {
    z := z + v * dt;
    v := v + a * dt;
    t := t + dt }
}
```

$\{z < m\}$

accel.

brake

```
while (v > 0 && m - z >= s) {
  a := a0; t := 0;
  while (t < eps && v > 0) {
    z := z + v * dt;
    v := v + a0 * dt;
    t := t + dt }
  while (v > 0 && m - z < s) {
    a := -b; t := 0;
    while (t < eps && v > 0) {
      z := z + v * dt;
      v := v - b * dt;
      t := t + dt }
}
```

$\{z < m\}$

Strategy 1 "Phase split"

[Sharma, Dillig, Dillig, Aiken; CAV'11]
 [Balakrishnan, Sankaranarayanan, Ivancic, Gupta; EMSOFT'09]
 [Gopan, Reps; SAS'07]

Hasuo (Tokyo)


```

while (v > 0 && m - z >= s) {
  a := a0; t := 0;
  while (t < eps && v > 0) {
    z := z + v * dt;
    v := v + a0 * dt;
    t := t + dt }};
while (v > 0 && m - z < s) {
  a := -b; t := 0;
  while (t < eps && v > 0) {
    z := z + v * dt;
    v := v - b * dt;
    t := t + dt }}

```

{z < m}

```

if (v > 0)
  then
    while (m - z >= s) {
      a := a0; t := 0;
      while (t < eps) {
        z := z + v * dt;
        v := v + a0 * dt;
        t := t + dt }}
    else skip;
  while (v > 0) {
    a := -b;

```

Strategy 4
"Differential invariant"

[Platzer,Clarke; CAV'08]

Strategies 2,3

"Superfluous guard elim." "Time elapse"

```

if (v > 0)
  then
    while (m - z >= s) {
      a := a0; t := 0;
      while (t < eps) {
        z := z + v * dt;
        v := v + a0 * dt;
        t := t + dt }}
    else skip;
  while (v > 0) {
    a := -b;
    z := z + v * dt;
    v := v - b * dt }

```

{z < m}

Strategy 5

"QE Invariant"

```

if (v > 0)
  then
    while (m - z >= s) {
      a := a0; t := 0;
      while (t < eps) {
        z := z + v * dt;
        v := v + a0 * dt;
        t := t + dt }}
    else skip;
    { (v > 0 ∨ m > z) ∧
      { (b²dt² + 4bdtv + 8bz + 4v² < 8bm)
        ∨ bdtv + 2bz + v² ≤ 2bm)
    }
  while (v > 0) {
    a := -b;
    z := z + v * dt;
    v := v - b * dt }

```

QE Invariant

Lem. In HOARE^{dt},

$$\vdash \left\{ \begin{array}{l} (\neg b \Rightarrow A) \wedge \\ \forall y \in \mathbb{N}. ((b[a/x]^y \wedge \neg b[a/x]^{y+1}) \Rightarrow A[a/x]^{y+1}) \end{array} \right\} \text{while } b \text{ do } x := a \{A\} .$$

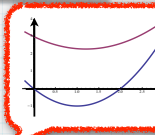
quantifier must go!
(to manage complexity)

* Quantifier elimination

* Tarski, CAD algorithm, Resolve in Mathematica

* e.g. $\models \forall x \in \mathbb{R}. (x^2 + ax + b > 0) \iff a^2 - 4b < 0$

* then $\models \forall x \in \mathbb{R}. (x^2 + ax + b > 0) \iff a^2 - 4b < 0$



by transfer!

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```

if (v > 0)
  then
    while (m - z >= s) {
      a := a0; t := 0;
      while (t < eps) {
        z := z + v * dt;
        v := v + a0 * dt;
        t := t + dt }}
    else skip;
  while (v > 0) {
    a := -b;
    z := z + v * dt;
    v := v - b * dt }

```

{z < m}

Strategy 5

"QE Invariant"

```

if (v > 0)
  then
    while (m - z >= s) {
      a := a0; t := 0;
      while (t < eps) {
        z := z + v * dt;
        v := v + a0 * dt;
        t := t + dt }}
    else skip;
    { (v > 0 ∨ m > z) ∧
      { (b²dt² + 4bdtv + 8bz + 4v² < 8bm)
        ∨ bdtv + 2bz + v² ≤ 2bm)
    }
  while (v > 0) {
    a := -b;
    z := z + v * dt;
    v := v - b * dt }

```

```

if (v > 0)
  then
    while (m - z >= s) {
      a := a0; t := 0;
      while (t < eps) {
        z := z + v * dt;
        v := v + a0 * dt;
        t := t + dt }}
    else skip;
    (v > 0 ∨ m > z) ∧
    { (b²dt² + 4b²tv + 8bz + 4v² < 8bm)
      ∨ b²tv + 2bz + v² ≤ 2bm)
    while (v > 0) {
      a := -b;
      z := z + v * dt;
      v := v - b * dt }
  }

```

+ some fwd. propagation

```

{ ... (long fml. with dt) }
while (m - z >= s) {
  a := a0; t := 0;
  while (t < eps) {
    z := z + v * dt;
    v := v + a0 * dt;
    t := t + dt }}
{ ... }
while (v > 0) {
  a := -b;
  z := z + v * dt;
  v := v - b * dt }

```

iteration: $x0/a$ times?

- * approximated by $\lfloor x0/a \rfloor$ or $\lceil x0/a \rceil$
- * \rightarrow monotonicity reqm. must be discharged

Strategy 6
"Iteration count"

```

x := 0;
while (x < x0) {
  x := x + a
}

```

```

{ ... (long fml. with dt) }
while (m - z >= s) {
  a := a0; t := 0;
  while (t < eps) {
    z := z + v * dt;
    v := v + a0 * dt;
    t := t + dt }}
{ ... }
while (v > 0) {
  a := -b;
  z := z + v * dt;
  v := v - b * dt }

```

long fml. w/o dt, whose core is

$$a_0(2\epsilon\sqrt{2a_0(m-s-z_0)+v_0^2+b\epsilon^2+2m-2s-2z_0} + 2b\epsilon\sqrt{2a_0(m-s-z_0)+v_0^2+a_0^2\epsilon^2+v_0^2} < 2bs)$$

the final outcome

Lem. If:

1. a is closed
2. $r \mapsto \llbracket a[r/dt] \rrbracket$ is continuous at $r = 0$,

then $\models a[0/dt] < 0 \implies a < 0$.

Strategy 7
"Cast to shadow"

(Eliminates dt, strengthens the precondition.)

Prototype Automatic Prover

P (While^{dt} program) \rightarrow **Hoare^{dt} Analyzer** \rightarrow **A** (precondition) s.t. $\vdash \{A\}P\{B\}$

B (postcondition)

VC generator (Frontend, in OCaml)

Symbolic Comp. Engine (Backend, in Mathematica)

Totally symbolic (crucial for transfer)

- * Fujitsu HX600 with Quad Core AMD Opteron 2.3GHz CPU, 32GB memory. Mathematica 7.0 for Linux x86 (64-bit)
- * ETCS: 40.96 sec.
- * Bouncing ball: runs with one manual insertion of invariants

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Related Work

- * **Deductive verification** of hybrid sys. [Platzer, '10] [Platzer, LICS'12]
- * Automatic prover KeYmaera
- * **Static analysis techniques**
- * A LOT in CAV, SAS, VMCAI, ...
- * Applied to hybrid systems (w/ diff. eq.) [Rodriguez-Carbonell, Tiwari; HSCC'05] [Sankaranarayanan; HSCC'10] [Sankaranarayanan, Sipma, Manna; Formal Methods Sys. Design '08]
- * **Use of NSA for hybrid systems** [Benveniste, Bourke, Caillaud, Pouzet; J. Comput. Syst. Sci. '12] [Bliudze, Krob; Fundam. Inform. '09] [Gamboa, Kaufmann; J. Autom. Reason. '01]
- * **Continuous techniques applied to discrete appl.** [Chaudhuri, Gulwani, Lublinerman, NavidPour; FSE '11]
- * Not contending! Combination?

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Conclusions

Nonstandard Static Analysis

While^{dt}

Programming lang.

```
while (t<a) do {  
  t:=t+1;  
  if ...  
}
```

Assn^{dt}

First-order assertion
lang.

$\exists z(x=2*z \wedge y=3*z)$

Hoare^{dt}

Hoare-style program
logic

$$\frac{\{A \wedge b\} c \{A\}}{\{A\} \text{ while } b \text{ do } c \{A \wedge \neg b\}}$$

Rigorous semantics by non-standard analysis

- * Tool's effectivity. More heuristics?
- * (Any discrete frmwk.)^{dt} ?
- * Simulink as stream processing?
- * With (explicit) differential equations?

Thank you for your attention!

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Hasuo (Tokyo)